

Inventec Corporation
R&D Division

CONFIDENTIAL

Board name : Mother Board Schematic

Project : xxxx (FSC_v5535)

Version : 01

Initial Date : April ,7 , 2007

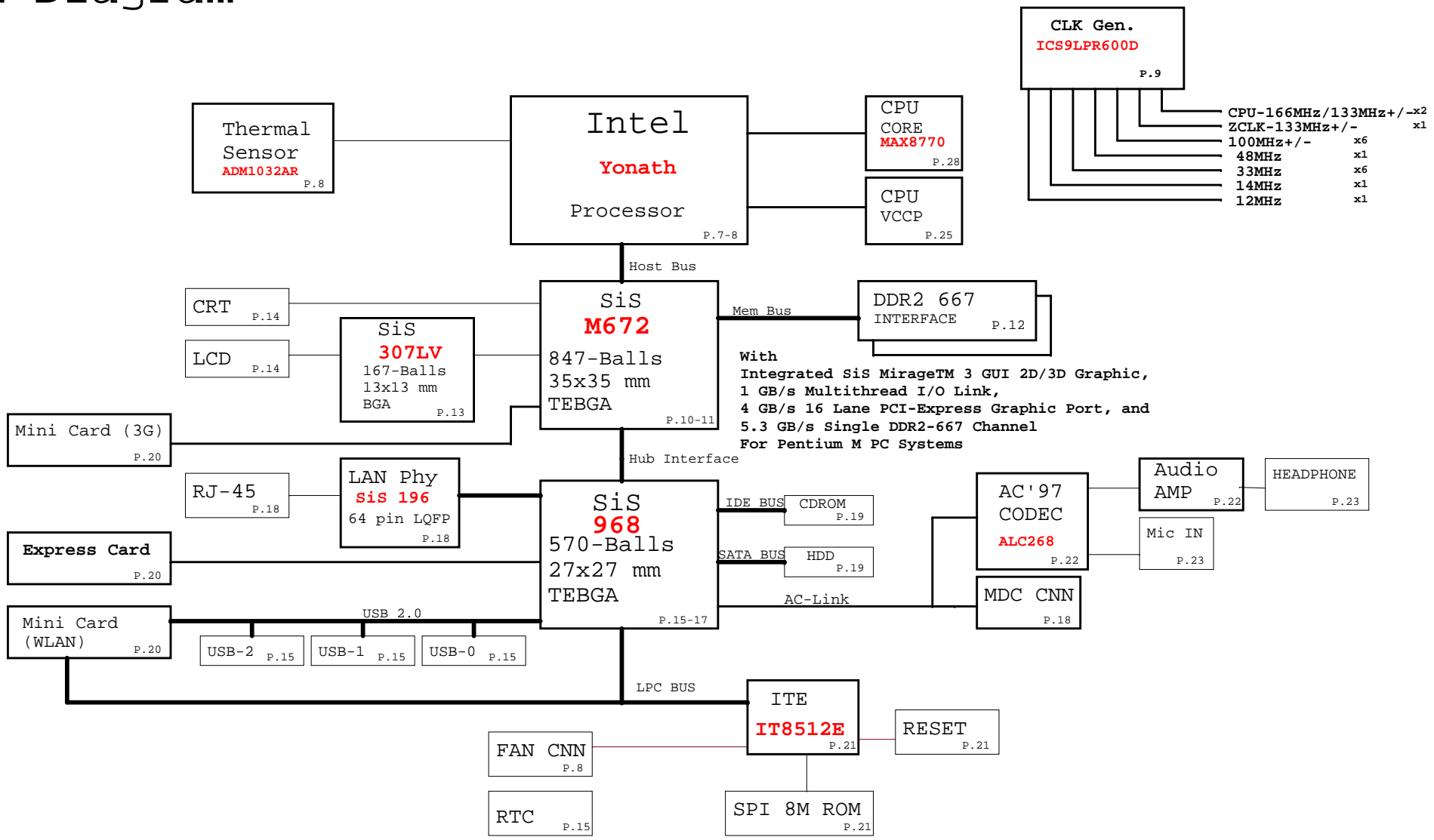
Inventec Corporation		
5F, No. 35, Section 2, Zhongyang South Road Beitou District, Taipei 11270, Taiwan TEL: +886-2-2861-0721		
<small><OrigAddress></small>		
Title		
<small>Size</small>	<small>Document Number</small>	<small>Rev</small>
<small>Customer/Doc</small>		<small>A02</small>
<small>Date</small>	<small>Friday, June 15, 2007</small>	<small>Sheet 1 of 31</small>

1. Schematic Page Description :

Schematic Ver : 01

- | | |
|-------------------------------------|--------------------------------------|
| 01. Title | 21. KBC-ITE-8512 |
| 02. Schematic Page Description | 22. AUDIO ALC268/AMP |
| 03. Block Diagram | 23. Audio JACK |
| 04. ANNOTATIONS | 24. 5VA/5VLA/3VA/3VLA |
| 05. Schematic Modify | 25. 5/3VS/1.5VA/1.5VS/VCCP |
| 06. Power Block Diagram | 26. DDR POWER (TPS51117)/1.8VA/1.8VS |
| 07. Yonath Processor (1/2) | 27. 1.2VA/1.2VS |
| 08. Yonath Processor (2/2) | 28. CPU Core Power (MAX8770) |
| 09. Clock Generator(ICS9PR600DGLFT) | 29. AC-IN & BATT POWER |
| 10. SIS672MX (1/2) | 30. USB BOARD |
| 11. SIS672MX (2/2) | 31. GP S/W BOARD |
| 12. DDRII-DIMM 0-1 | |
| 13. SiS307LV | |
| 14. CRT& LCD | |
| 15. SiS 968 (1/3) | |
| 16. SiS 968 (2/3) | |
| 17. SiS 968 (3/3) | |
| 18. LAN (SiS196) & MDC | |
| 19. HDD & CDROM | |
| 20. 3G/Mini/New card | |
-

3. Block Diagram :



4. Nat name Description :

Voltage Rails

DCIN Primary DC system power supply
 5VLA 5.0V always on power rail by LATCH or ACIN
 3VLA 3.0V always on power rail by LATCH or ACIN
 5VA 5.0V always on power rail by ECPWON
 3VA 3.3V always on power rail by ECPWON
 1.8VA 1.8V always on power rail
 1.5VA 1.5V always on power rail
 1.2VA 1.2V always on power rail

5VS 5.0V switched power rail by SLP_S3#
 3VS 3.3V switched power rail by SLP_S3#
 1.8VS 1.8V switched power rail by SLP_S3#
 1.5VS 1.5V switched power rail by SLP_S3#
 1.2VS 1.2V switched power rail by SLP_S3#

1.8V 1.8V DDR Voltage
 +V0.9_DDR 0.9V DDR Termination Voltage

Vccp AGTL+ Voltage for CPU
 Vcore_CPU Core Voltage for CPU
 2.5V (LAN) 2.5V power rail for LAN

Part Naming Conventions

C = Capacitor
 CN = Connector
 D = Diode
 F = Fuse
 L = Inductor
 Q = Transistor
 R = Resistor
 RS = Resistor Pack
 U = Arbitrary Logic Device
 Y = Crystal and Osc

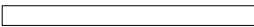





Net Name Suffix

0 or # = Active Low signal

5.Board Stack up Description

PCB Layers

PCB Thickness :1.2mm+/-0.1 mm .

Layer 1  Component Side, Microstrip signal Layer
 Layer 2  Power Plane
 Layer 3  Stripline Layer (AGTL, CLOCK, DDR)
 Layer 4  Stripline Layer (Analog, LVDS, other)
 Layer 5  Ground Plane
 Layer 6  Solder Side, Microstrip signal Layer

Power Rail	Destination	Voltage	SO Current
VCC_CORE	Merom HFM: LFM:	1.3319V-1.4375V-1.4591V 0.9221V-0.9625V-0.9739V	36A
+1.05VS	Merom: AGTL+ termination SIS762 SIS968	0.997V-1.05V-1.102V 1.0V-1.05V-1.1V	2.5A 80mA 22mA
+1.8V	SIS762	1.7V-1.8V-1.9V	664mA
0.9VDDT_DDRII	SO-DIMM: Terminator:	0.855V-0.9V-0.945V	4.0A 1.0A
1.2VS	SIS968		2.69A
+1.5VS	Merom PLL Mini Card: WirelessLan Express Card:	1.425V-1.5V-1.575V	120mA 500mA 650mA
1.8VS	SIS762		600mA
	SIS968		816mA
	SIS307ELV		389mA
3VS	SIS968 LCD: Mini Card: UMTS Express Card: CLK Generator: ICS9LPRS365AGLF Mini Card: WirelessLan	3.135V-3.3V-3.465V 3.0V-3.3V-3.6V	74mA 1.5A 600mA 1.3A 400mA 1.0A
	Azalia Codec: ALC268 Azalia MDC: HDD: SATA LED: SIS307ELV	3.0V-3.3V-3.6V 3.0V-3.3V-3.6V	25mA 30mA 60mA 132mA
	LED: Azalia Codec: ALC268 FAN: HDD: SATA ODD: PATA Audio AMP: G1432 Inverter:	3.0V-3.3V-3.6V 4.75V-5.0V-5.25V 4.75V-5.0V-5.25V	140mA 35mA Max: 1.0A ; R/W: 460mA ; STDBY: 70mA Max: 1.8A ; R/W: 900mA ; STDBY: 45mA
1.2VA	SIS762		69mA
1.5VA	SIS196		393mA
1.8VA	SIS762		23mA
	SIS968		327mA
2.5VA	SIS196	2.32V-2.5V-2.625V	223mA
+3VA	Thermal Sensor: SIS968 SIS968: RTC Express Card:		8mA 275mA
+5VA	USB: x 3 ports	5VA	1.5A
3VLA	EC: ITE8512F		
+5VLA	Control Power		

Inventec Corporation
 5F, No. 35, Section 2, Zhongyuan South Road
 Beitou District, Taipei 11270, Taiwan
 TEL: +886-2-2881-0721

OrgAddress:
 Title:
ANNOTATIONS
 Size: Document Number:
 Cust: Date: Rev:
 Date: Friday, June 15, 2007 Sheet 4 of 31

6.Schematic modify Item and History :

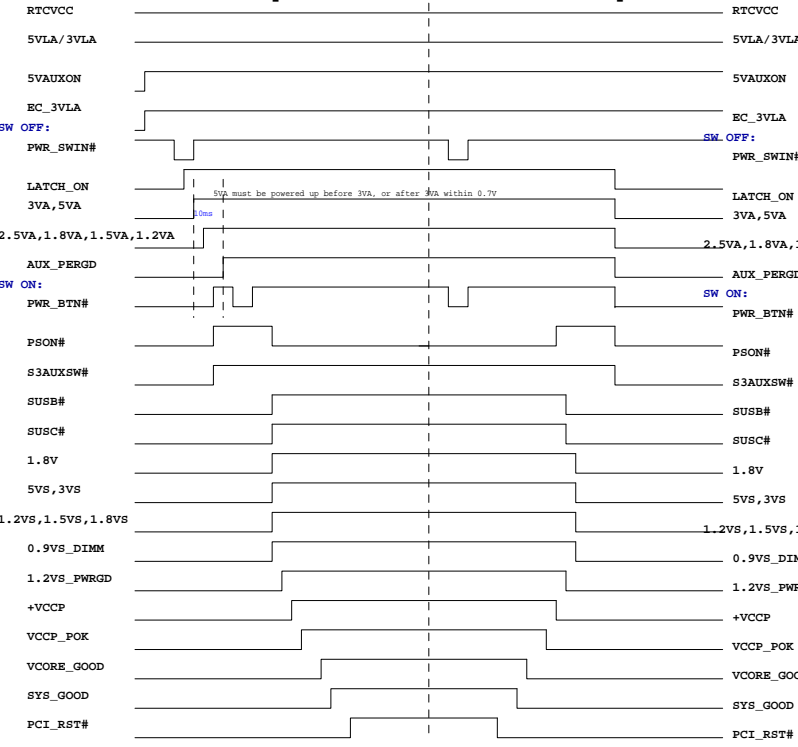
release version 0.2
1.REMOVE R471,C584,C586,R450,R449,R359,R92,R365
2.R439 FULL DOWN
3.L57 MODIFY
4.R459 modify-->7.5k
5.ADD R533
6.R301 ---modify 10k
7.C418,R311 NU
8.R312 MODIFY--220K
9.BATIN MODIFY TO PIN 17
10.ADD C107
11.F1 MODIFY TO 5A
12.R309 MODIFY 10K
13.MODIFY L57,L56 TO SIZE6*6*3

Inventec Corporation		
5F, No. 35, Section 2, Zhongyang South Road Beikou District, Taipei 11270, Taiwan TEL:+886-2-2861-0721		
-OrigAddress		
Title		
Schematic Modify		
Size	Document Number	Rev
Customer/Doc		A02
Date:	Friday, June 15, 2007	Sheet 5 of 31

SYSTEM POWER ON/OFF SEQUENCE

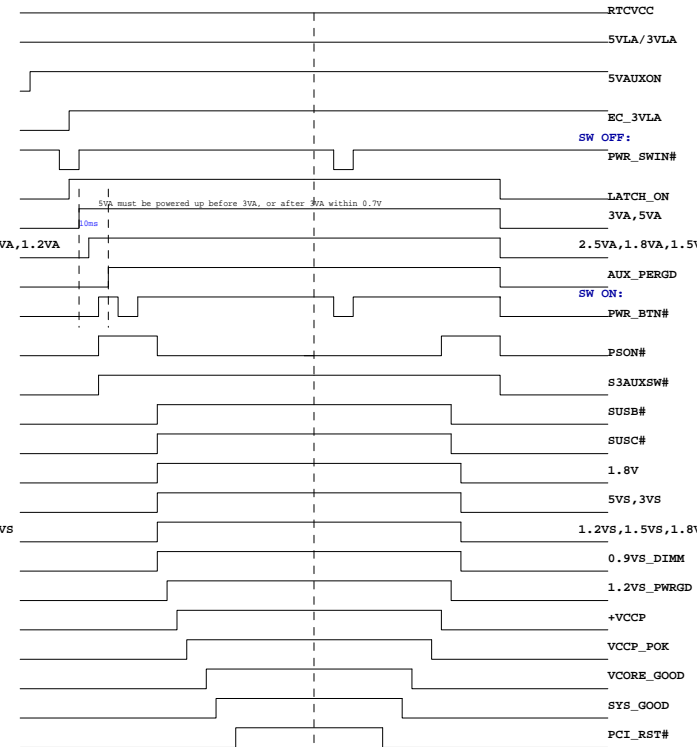
Power on/off sequence AC insert(First)

Power on sequence Power off sequence



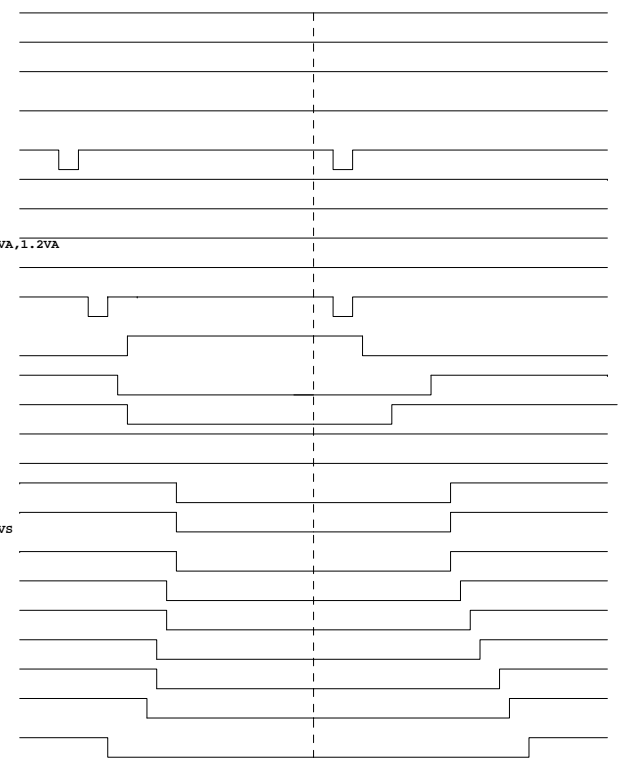
Battery only Power on/off sequence

Power on sequence Power off sequence



Suspend resume sequence(S3)

Power on sequence Power off sequence



Inventec Corporation

5F, No. 35, Section 2, Zhongyuan South Road
Beitou District, Taipei 11270, Taiwan
TEL: +886-2-2861-0721

OrigAddress

Title

Power Block Diagram

Size

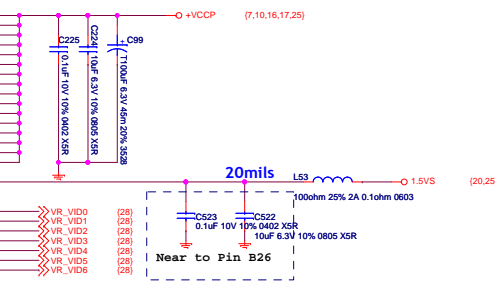
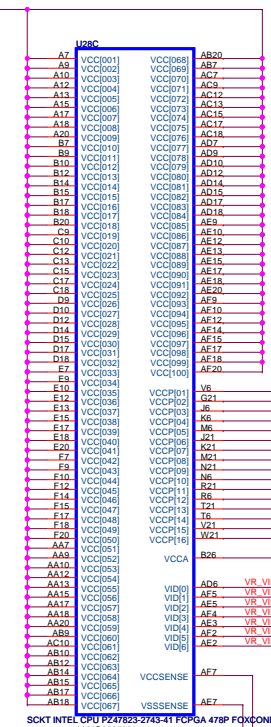
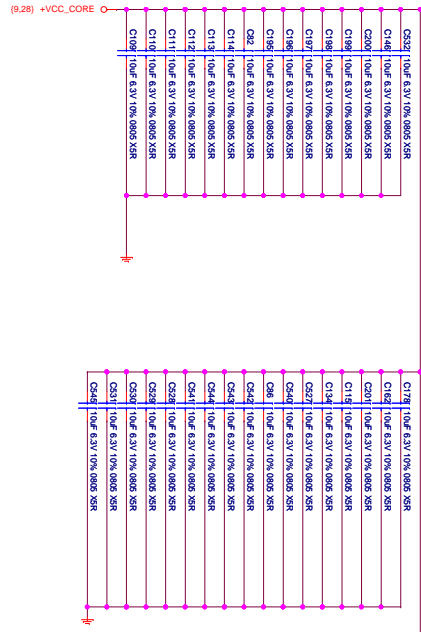
Document Number

Rev

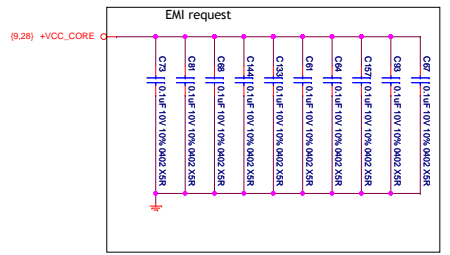
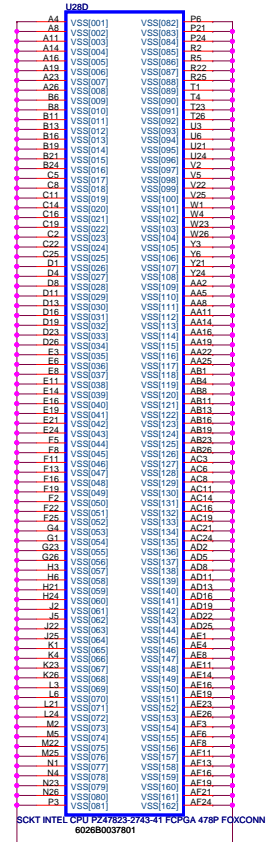
CustomerDoc

Date: Friday, June 15, 2007

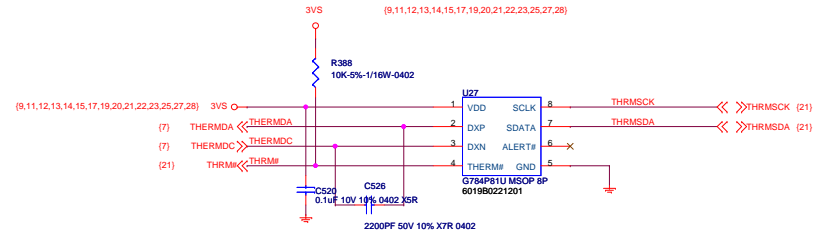
Sheet 6 of 31



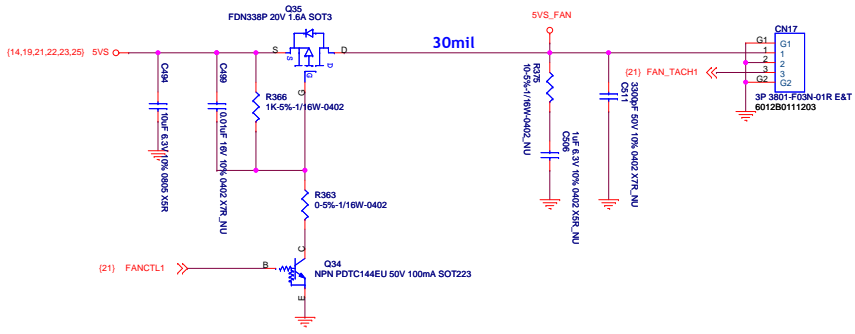
Route VCCSENSE and VSSSENSE traces at 27.4 ohms with 50mil spacing. Place PU and PD within 1 inch of CPU



THERMAL SENSOR

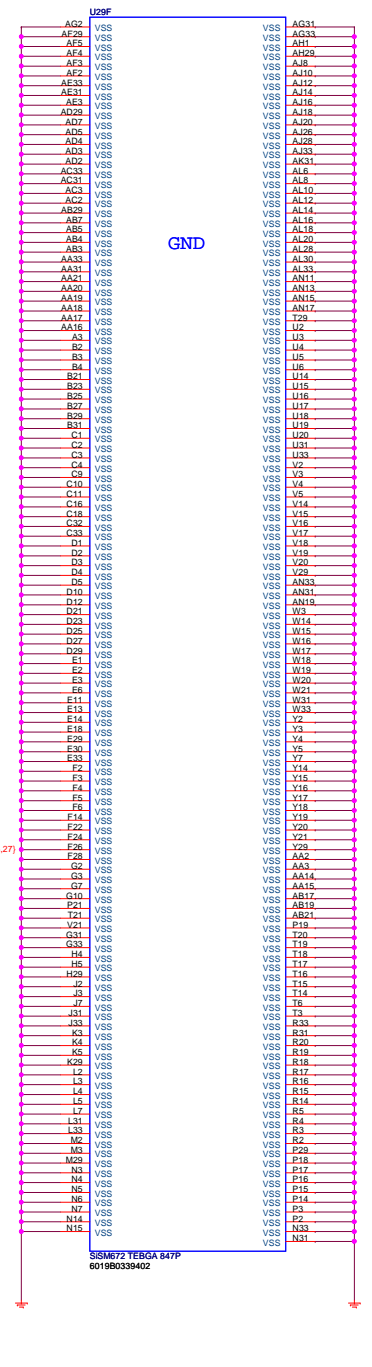
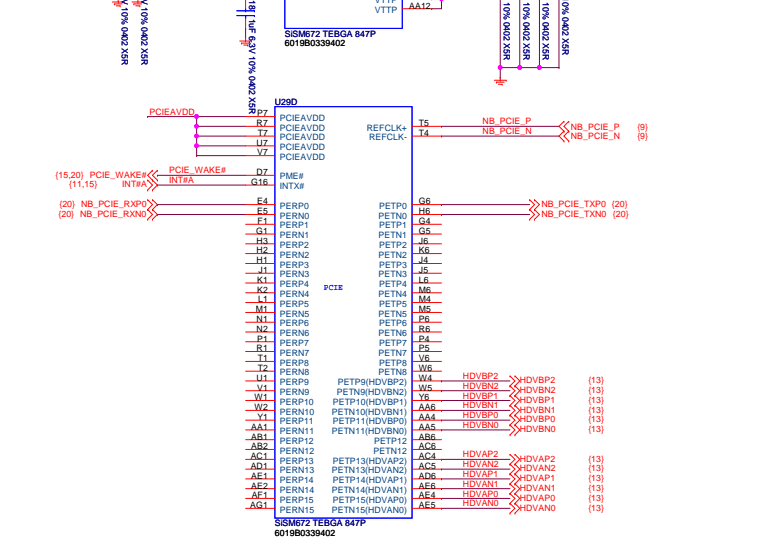
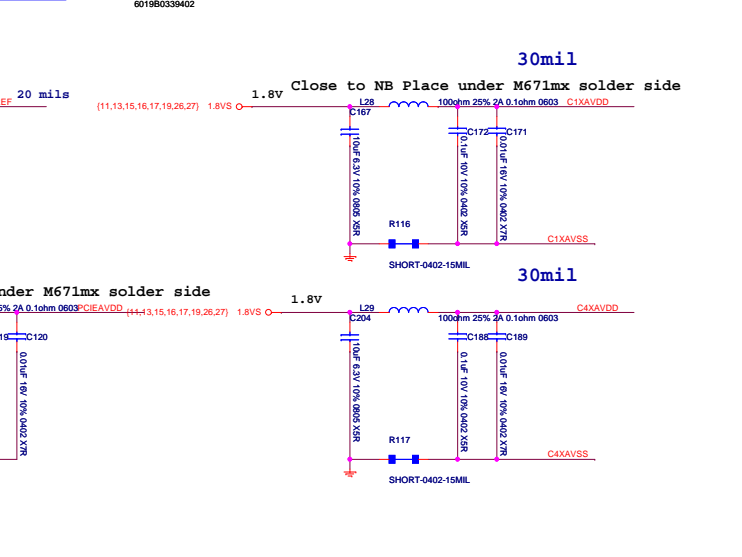
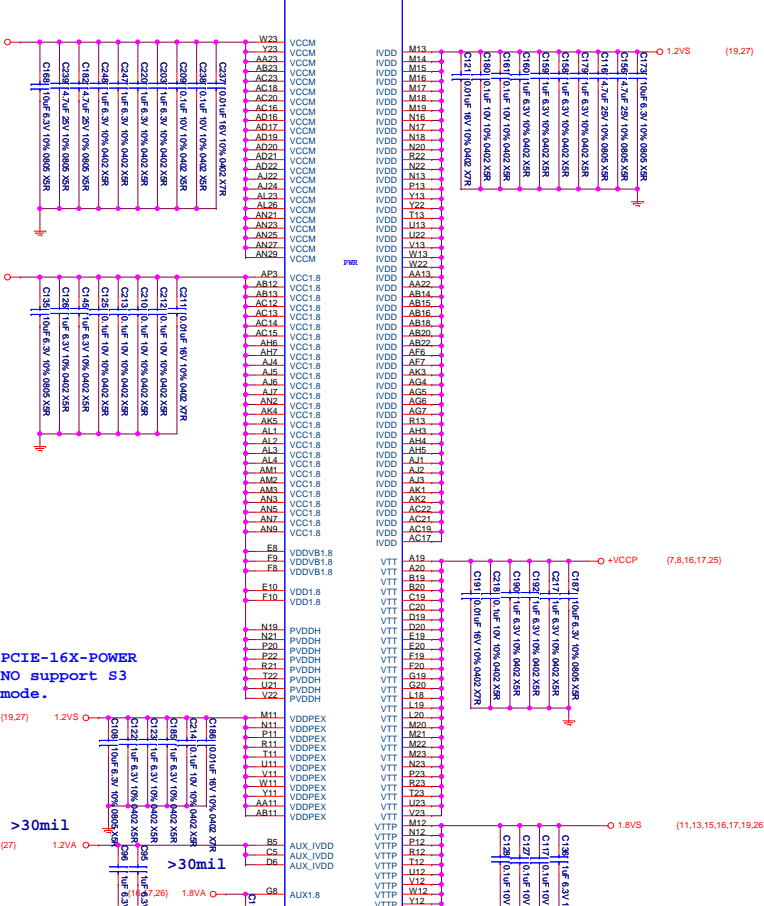
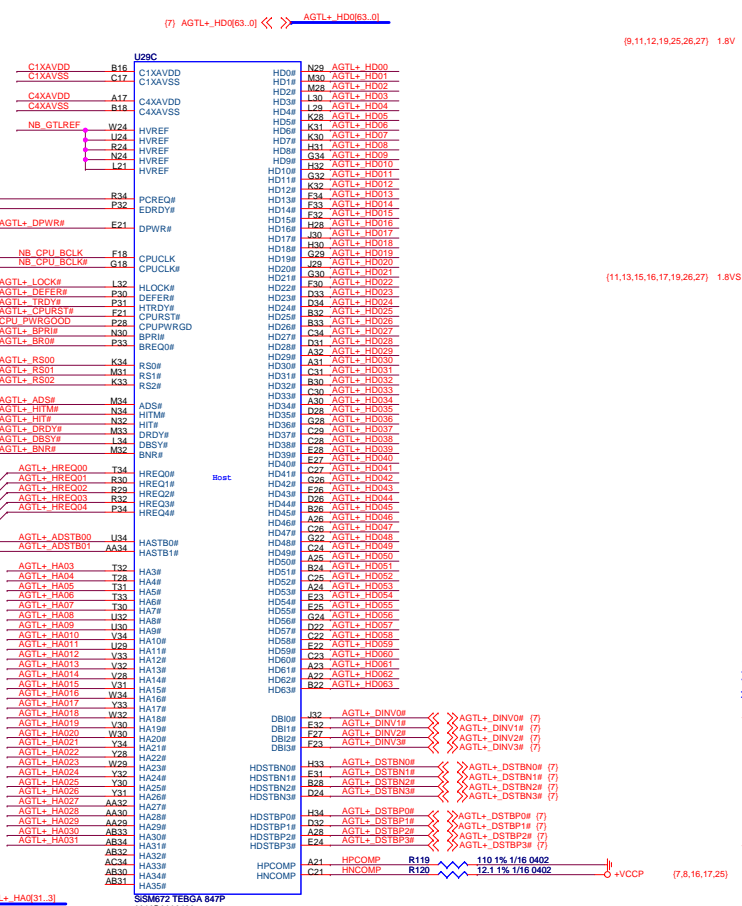


Fan control



Inventec Corporation
 5F, No. 35, Section 2, Zhongyong South Road
 Beitou District, Taipei 11270, Taiwan
 TEL:+886-2-2861-0721

File: **Yonath Processor (2/2)**
 Size: Document Number
 CustomDoc:
 Date: Friday, June 15, 2007 Sheet 8 of 31



Inventec Corporation

5F, No. 35, Section 2, Zhongyong South Road
 Beitou District, Taipei 11270, Taiwan
 TEL: +886-2-2861-0721

File: **SIS672MX (1/2)**

Size: Document Number
 Customer Doc: _____

Date: Friday, June 15, 2007 Sheet 10 of 31

Close to NB Place under M671mx solder side

Close to NB Place under M671mx solder side

PCIE-16X-POWER
 NO support s3 mode.

>30mil

30mil

30mil

GND

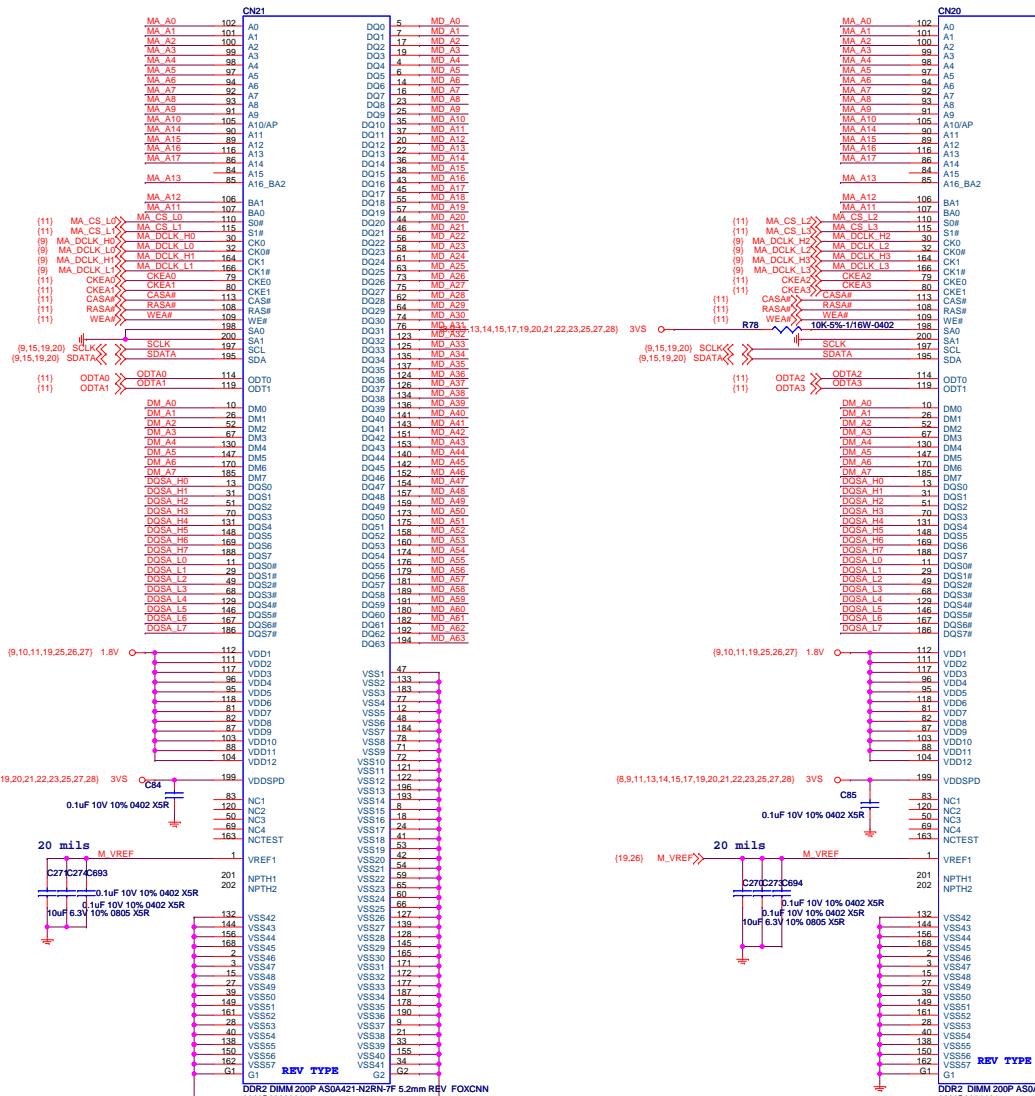
GND

SO-DIMMO

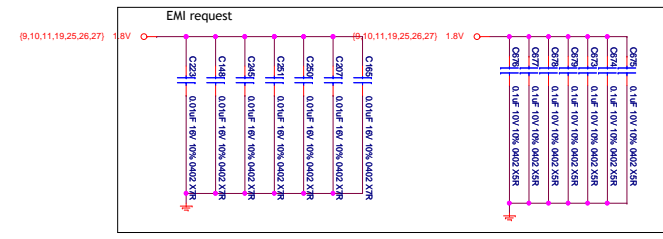
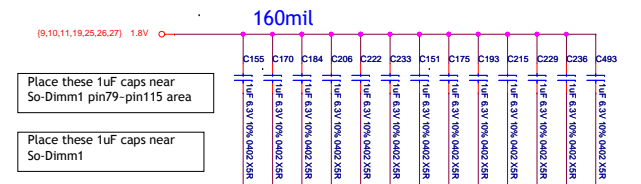
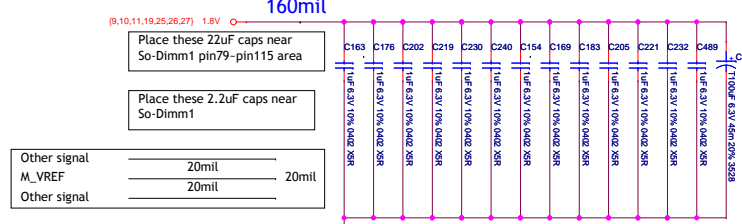
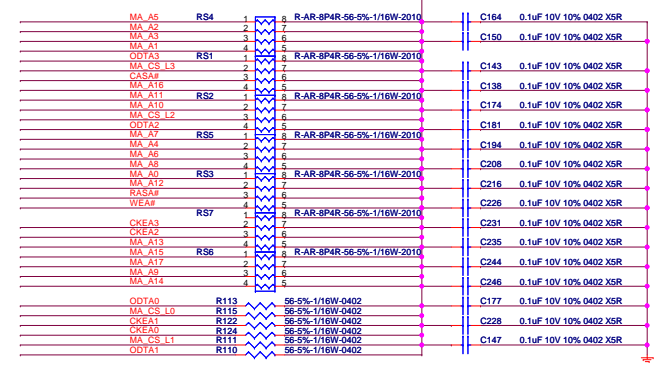
SO-DIMM1

Place one cap close to every 2 pullup resistors terminated to 0.9VDDT_DDRII

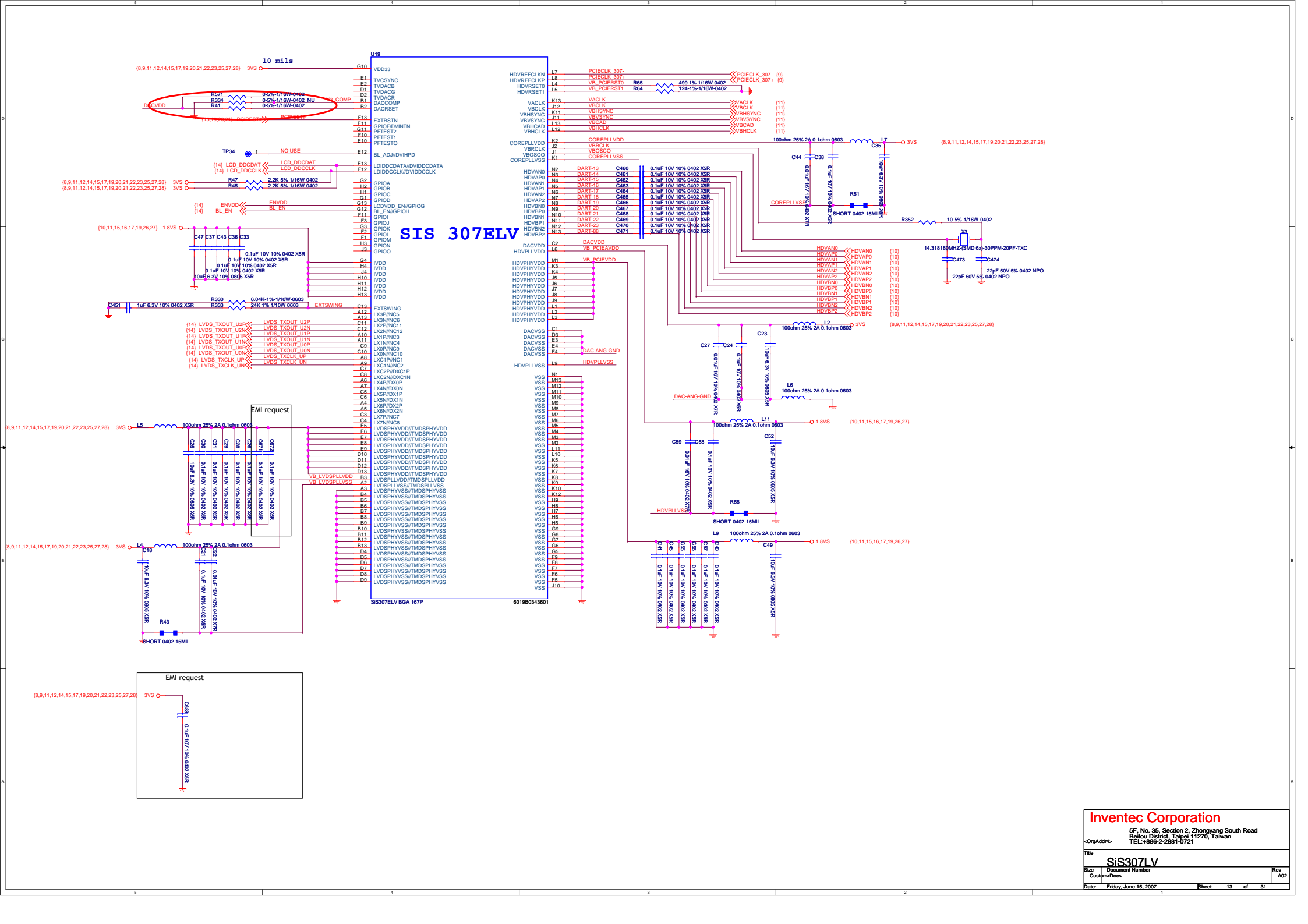
- (11) MD_A[63:0] << MD_A[63:0]
- (11) MA_A[17:0] << MA_A[17:0]
- (11) DM_A[7:0] << DM_A[7:0]
- (11) DQSA_H[7:0] << DQSA_H[7:0]
- (11) DQSA_L[7:0] << DQSA_L[7:0]



SO-DIMM 400 mils



Inventec Corporation
 5F, No. 35, Section 2, Zhongyong South Road
 Beitou District, Taipei 11270, Taiwan
 TEL:+886-2-2861-0721

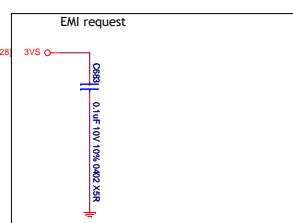


SIS 307ELV

SIS307ELV BGA 167P

601960343601

U19	G10	VDD33
	E1	TVCSYNC
	E2	TVDACB
	D1	TVDACG
	D2	TVDACR
	B1	DACC0MP
	B2	DACRSET
	F13	EXTRSTN
	E11	GPI0F/DVINTN
	G11	PFTST1
	E10	PFTST2
	G11	PFTST1
	E10	PFTST0
	E12	BL_ADJ/DIVHPD
	E13	LDDDDATA/DVDDCDA
	F12	LDDDDCLK/DVDDCCLK
	G2	GPI0A
	H2	GPI0B
	H1	GPI0C
	G1	GPI0D
	G12	LCD_VDD_ENI/GPI0G
	G13	BL_ENIGP0H
	F3	GPI0I
	G3	GPI0J
	F1	GPI0K
	F1	GPI0L
	H3	GPI0N
	H3	GPI0O
	G4	VDD
	H4	VDD
	J4	VDD
	H11	VDD
	H12	VDD
	H13	VDD
	C13	EXTSWING
	A13	LX3N/NC6
	C11	LX2N/NC1
	A10	LX1P/NC3
	A11	LX1N/NC4
	C9	LX0P/NC9
	A9	LX0N/NC10
	A8	LX0P/NC1
	A9	LX1N/NC2
	C7	LX2P/DXCP
	A6	LX2N/DXNP
	A7	LX4N/DXN
	C6	LX5P/DX1P
	A4	LX5N/DX1N
	A4	LX6P/DX2P
	A5	LX6N/DX2N
	C4	LX7P/INC7
	C4	LX7N/NC8
	E6	LVDSPHYVDD/TMDSPHYVDD
	E7	LVDSPHYVDD/TMDSPHYVDD
	E8	LVDSPHYVDD/TMDSPHYVDD
	D10	LVDSPHYVDD/TMDSPHYVDD
	D11	LVDSPHYVDD/TMDSPHYVDD
	D12	LVDSPHYVDD/TMDSPHYVDD
	D13	LVDSPHYVDD/TMDSPHYVDD
	B3	LVDSPHYVDD/TMDSPHYVDD
	A3	LVDSPHYVDD/TMDSPHYVDD
	B4	LVDSPHYVSS/TMDSPHYVSS
	B5	LVDSPHYVSS/TMDSPHYVSS
	B6	LVDSPHYVSS/TMDSPHYVSS
	B7	LVDSPHYVSS/TMDSPHYVSS
	B8	LVDSPHYVSS/TMDSPHYVSS
	B9	LVDSPHYVSS/TMDSPHYVSS
	B10	LVDSPHYVSS/TMDSPHYVSS
	B11	LVDSPHYVSS/TMDSPHYVSS
	B12	LVDSPHYVSS/TMDSPHYVSS
	B13	LVDSPHYVSS/TMDSPHYVSS
	D6	LVDSPHYVSS/TMDSPHYVSS
	D7	LVDSPHYVSS/TMDSPHYVSS
	D8	LVDSPHYVSS/TMDSPHYVSS
	D9	LVDSPHYVSS/TMDSPHYVSS



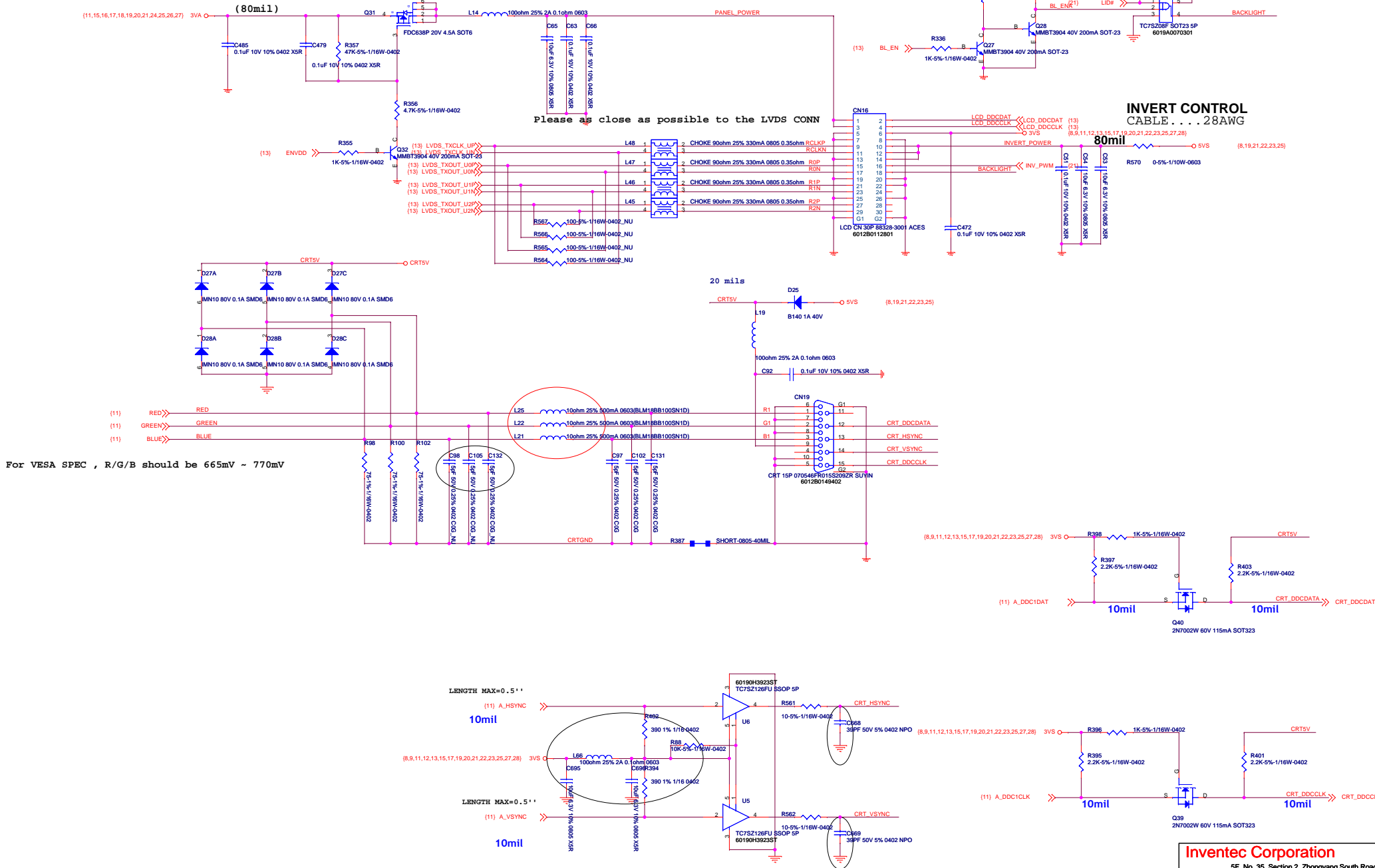
Inventec Corporation
 5F, No. 35, Section 2, Zhongyuan South Road
 Beitou District, Taipei 11270, Taiwan
 TEL:+886-2-2861-0721

File: **SIS307LV**
 Size: Document Number
 CustomDoc:
 Date: Friday, June 15, 2007 Sheet 13 of 31

LCD brightness control

LVDS Interface

INVERT CONTROL CABLE 28AWG



For VESA SPEC , R/G/B should be 665mV ~ 770mV

Inventec Corporation
 5F, No. 35, Section 2, Zhongyong South Road
 Beitou District, Taipei 11270, Taiwan
 TEL:+886-2-2861-0721

Originals: _____
 Title: **CRT & LCD**
 Size: Document Number _____
 Custom: Doc# _____
 Date: Friday, June 15, 2007 Sheet 14 of 31

(7)	CPU_INT#	CPU_INT#	AC23C	INT#
(7)	CPU_AGM#	CPU_AGM#	A20M	AGM#
(7)	CPU_SMI#	CPU_SMI#	AD22C	SMI#
(7)	CPU_INTR	CPU_INTR	AC22C	INTR
(7)	CPU_MMIO#	CPU_MMIO#	AE22C	MMIO#
(7)	CPU_IGNNE#	CPU_IGNNE#	AE24C	IGNNE#
(7)	CPU_FERR#	CPU_FERR#	AE24C	FERR#
(7)	CPU_STPCLK#	CPU_STPCLK#	AE24C	STPCLK#
(7)	CPU_SLP#	CPU_SLP#	AD24C	CPUSLP#
(7)	AGPBUSY#	AGPBUSY#	AD24C	MBUSY#

(7,28)	PROCHOT#	PROCHOT#	AC24C	PROCHOT#
(7,21)	THERMTRIP#	THERMTRIP#	AC24C	THERMTRIP#

CPU_S GPIO

APIC

PCI

LPC

SATA

IDE

MuTIOL

SPI

ACPI/Others

PCI

LPC

SATA

IDE

SPI

MuTIOL

APIC

CPU_S GPIO

ACPI/Others

PCI

LPC

SATA

IDE

MuTIOL

SPI

APIC

CPU_S GPIO

ACPI/Others

PCI

LPC

SATA

IDE

MuTIOL

SPI

APIC

CPU_S GPIO

ACPI/Others

PCI

LPC

SATA

IDE

MuTIOL

SPI

APIC

CPU_S GPIO

ACPI/Others

PCI

LPC

SATA

IDE

MuTIOL

SPI

APIC

CPU_S GPIO

ACPI/Others

PCI

LPC

SATA

IDE

MuTIOL

SPI

APIC

CPU_S GPIO

ACPI/Others

PCI

LPC

SATA

IDE

MuTIOL

SPI

APIC

CPU_S GPIO

ACPI/Others

PCI

LPC

SATA

IDE

MuTIOL

SPI

APIC

CPU_S GPIO

ACPI/Others

PCI

LPC

SATA

IDE

MuTIOL

SPI

APIC

CPU_S GPIO

ACPI/Others

PCI

LPC

SATA

IDE

MuTIOL

SPI

APIC

CPU_S GPIO

ACPI/Others

PCI

LPC

SATA

IDE

MuTIOL

SPI

APIC

CPU_S GPIO

ACPI/Others

PCI

LPC

SATA

IDE

MuTIOL

SPI

APIC

CPU_S GPIO

ACPI/Others

PCI

LPC

SATA

IDE

MuTIOL

SPI

APIC

CPU_S GPIO

ACPI/Others

PCI

LPC

SATA

IDE

MuTIOL

SPI

APIC

CPU_S GPIO

ACPI/Others

PCI

LPC

SATA

IDE

MuTIOL

SPI

APIC

CPU_S GPIO

ACPI/Others

PCI

LPC

SATA

IDE

MuTIOL

SPI

APIC

CPU_S GPIO

ACPI/Others

PCI

LPC

SATA

IDE

MuTIOL

SPI

APIC

CPU_S GPIO

ACPI/Others

PCI

LPC

SATA

IDE

MuTIOL

SPI

APIC

CPU_S GPIO

ACPI/Others

PCI

LPC

SATA

IDE

MuTIOL

SPI

APIC

CPU_S GPIO

ACPI/Others

PCI

LPC

SATA

IDE

MuTIOL

SPI

APIC

CPU_S GPIO

ACPI/Others

PCI

LPC

SATA

IDE

MuTIOL

SPI

APIC

CPU_S GPIO

ACPI/Others

PCI

LPC

SATA

IDE

MuTIOL

SPI

APIC

CPU_S GPIO

ACPI/Others

PCI

LPC

SATA

IDE

MuTIOL

SPI

APIC

CPU_S GPIO

ACPI/Others

PCI

LPC

SATA

IDE

MuTIOL

SPI

APIC

CPU_S GPIO

ACPI/Others

PCI

LPC

SATA

IDE

MuTIOL

SPI

APIC

CPU_S GPIO

ACPI/Others

PCI

LPC

SATA

IDE

MuTIOL

SPI

APIC

CPU_S GPIO

ACPI/Others

PCI

LPC

SATA

IDE

MuTIOL

SPI

APIC

CPU_S GPIO

ACPI/Others

PCI

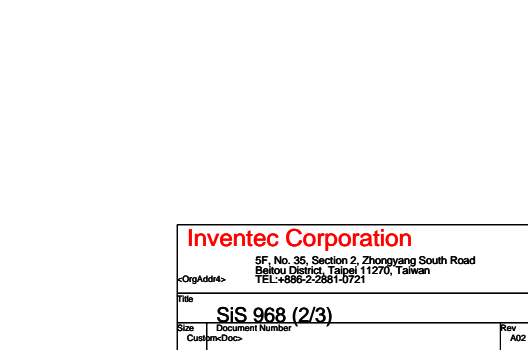
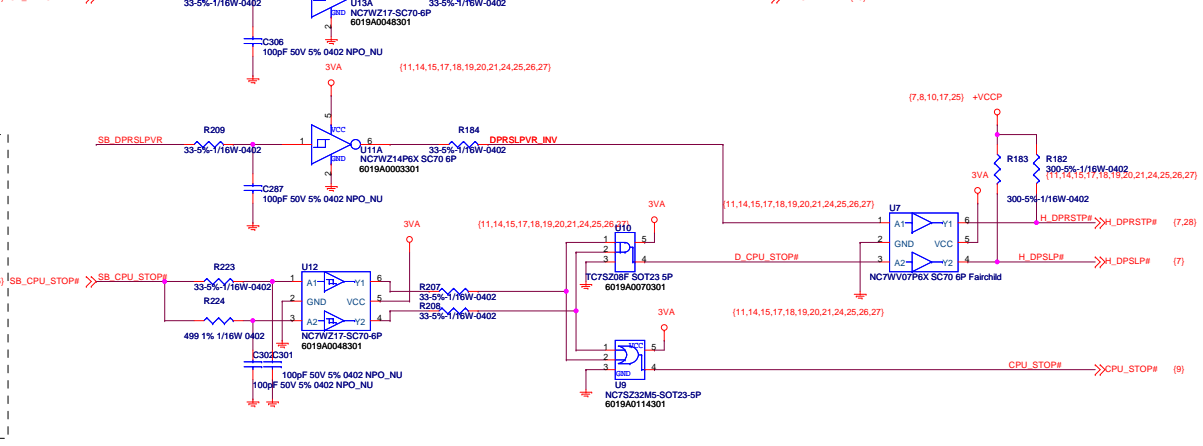
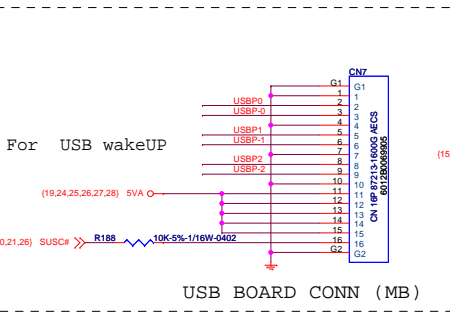
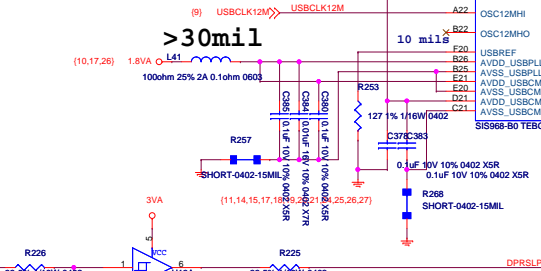
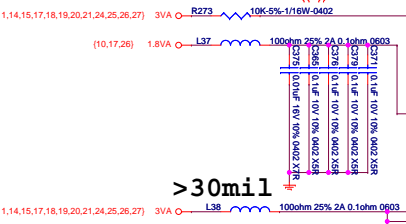
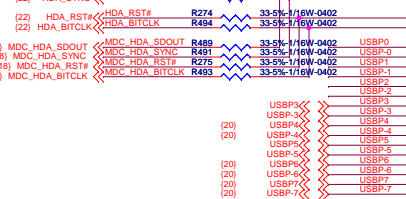
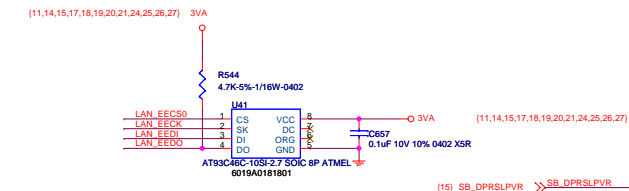
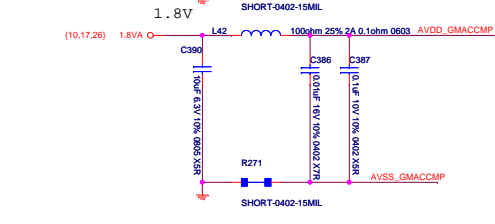
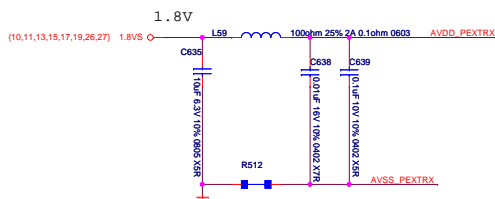
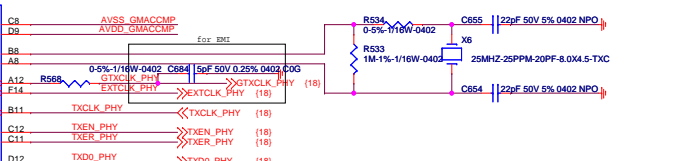
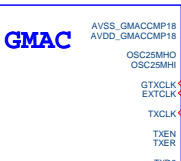
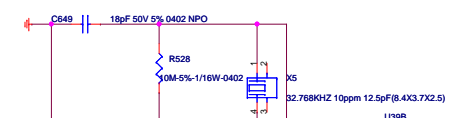
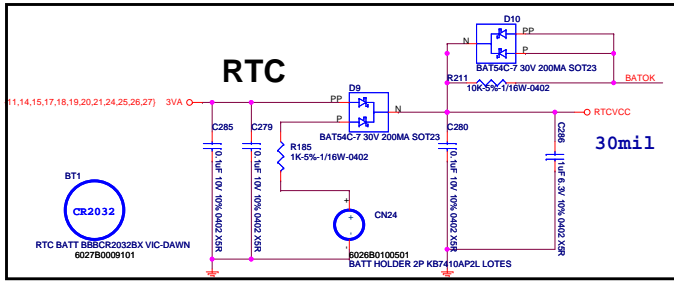
LPC

SATA

IDE

MuTIOL

The RTCVDD minimum standby voltage could be defined to 2.7V when V_3.3V_Dual is 0V.



Inventec Corporation

SF, No. 35, Section 2, Zhongyong South Road
Beitou District, Taipei 11270, Taiwan
TEL:+886-2-2861-0721

File: **Sis 968 (2/3)**

Size: Document Number

Customer: Doc

Date: Friday, June 15, 2007

Sheet 16 of 31

U99C

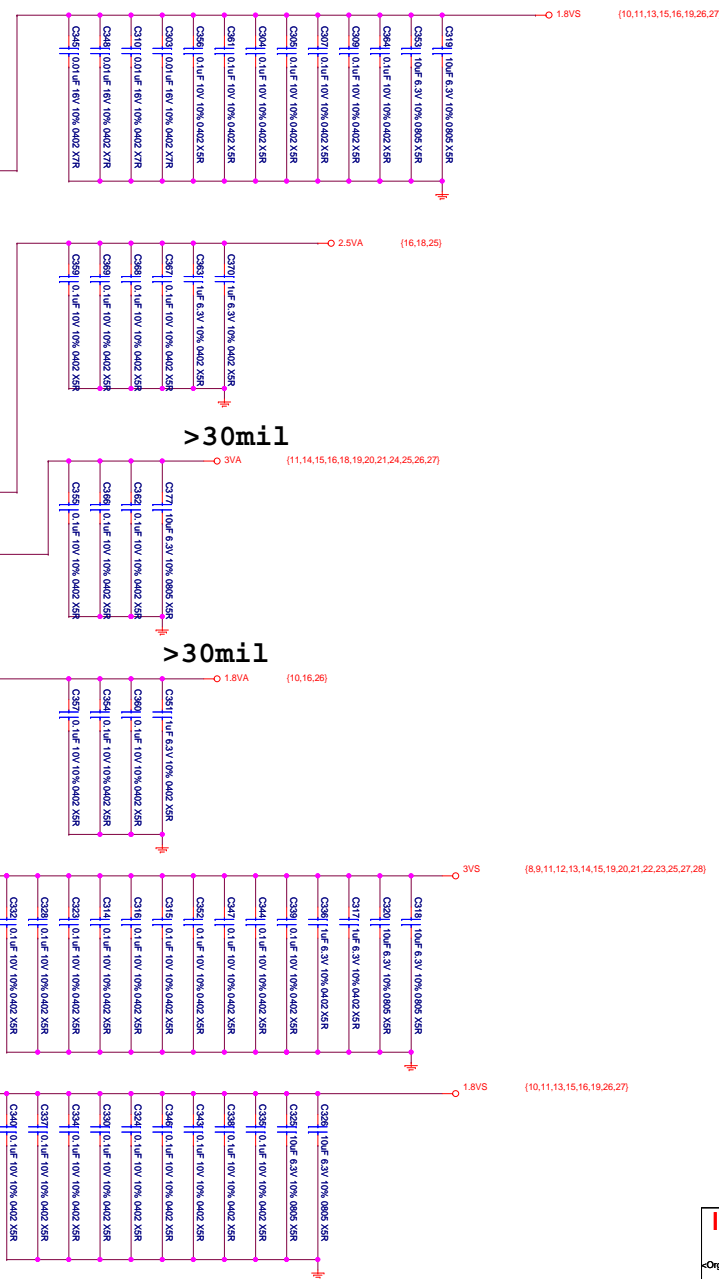
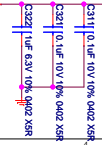
- AF13 AVSS_SATA
- AF11 AVSS_SATA
- AF9 AVSS_SATA
- AF8 AVSS_SATA
- AF4 AVSS_SATA
- AF3 AVSS_SATA
- AF2 AVSS_SATA
- AE14 AVSS_SATA
- AE13 AVSS_SATA
- AE11 AVSS_SATA
- AE9 AVSS_SATA
- AE7 AVSS_SATA
- AE6 AVSS_SATA
- AE4 AVSS_SATA
- AE3 AVSS_SATA
- AE2 AVSS_SATA
- AE1 AVSS_SATA
- AD14 AVSS_SATA
- AD12 AVSS_SATA
- AD7 AVSS_SATA
- AD5 AVSS_SATA
- AD4 AVSS_SATA
- AD3 AVSS_SATA
- AD2 AVSS_SATA
- AC16 AVSS_SATA
- AC14 AVSS_SATA
- AC7 AVSS_SATA
- AC5 AVSS_SATA
- AC4 AVSS_SATA
- AC3 AVSS_SATA
- AC2 AVSS_SATA
- AB15 AVSS_SATA
- AB14 AVSS_SATA
- AB13 AVSS_SATA
- AB12 AVSS_SATA
- AB7 AVSS_SATA
- AB6 AVSS_SATA
- L17 USBVSS
- L14 USBVSS
- K17 USBVSS
- K16 USBVSS
- K15 USBVSS
- K14 USBVSS
- K13 USBVSS
- C26 USBVSS
- C25 USBVSS
- D24 USBVSS
- D23 USBVSS
- B21 USBVSS
- A21 USBVSS
- C20 USBVSS
- E19 USBVSS
- B19 USBVSS
- A19 USBVSS
- D18 USBVSS
- C18 USBVSS
- E17 USBVSS
- B17 USBVSS
- A17 USBVSS
- D16 USBVSS
- C16 USBVSS
- P16 USBVSS
- A15 USBVSS
- E15 USBVSS
- D14 USBVSS
- AD26 VSS
- B25 VSS
- B10 VSS
- D11 VSS
- D13 VSS
- U16 VSS
- U14 VSS
- U13 VSS
- U12 VSS
- U11 VSS
- U10 VSS
- T14 VSS
- T13 VSS
- T12 VSS
- T11 VSS
- T10 VSS
- R14 VSS
- R13 VSS
- R12 VSS
- R11 VSS
- R10 VSS
- P14 VSS
- P13 VSS
- P12 VSS
- P11 VSS
- P10 VSS
- N14 VSS
- N13 VSS
- N12 VSS
- N11 VSS
- N10 VSS
- M15 VSS
- M14 VSS
- M13 VSS
- M12 VSS
- M11 VSS
- M10 VSS
- L16 VSS
- L15 VSS
- L14 VSS
- L12 VSS
- L11 VSS
- L10 VSS
- K12 VSS
- K11 VSS
- K10 VSS
- U17 VSSZ
- U16 VSSZ
- T16 VSSZ
- R16 VSSZ
- R15 VSSZ
- T17 VSSZ
- P15 VSSZ
- T15 VSSZ
- V21 VSSZ
- T1 VSSZ
- V21 VSSZ
- R22 VSSZ
- V22 VSSZ
- W23 VSSZ
- U23 VSSZ
- AA21 VTT
- AB22 VTT

SIS968-B0 TBEGA 570P
60198039901

Power / Ground

>30mil

(7,8,10,16,25) +VCCP



>30mil

>30mil

Inventec Corporation
 5F, No. 35, Section 2, Zhongyong South Road
 Beitou District, Taipei 11270, Taiwan
 TEL: +886-2-2881-0721

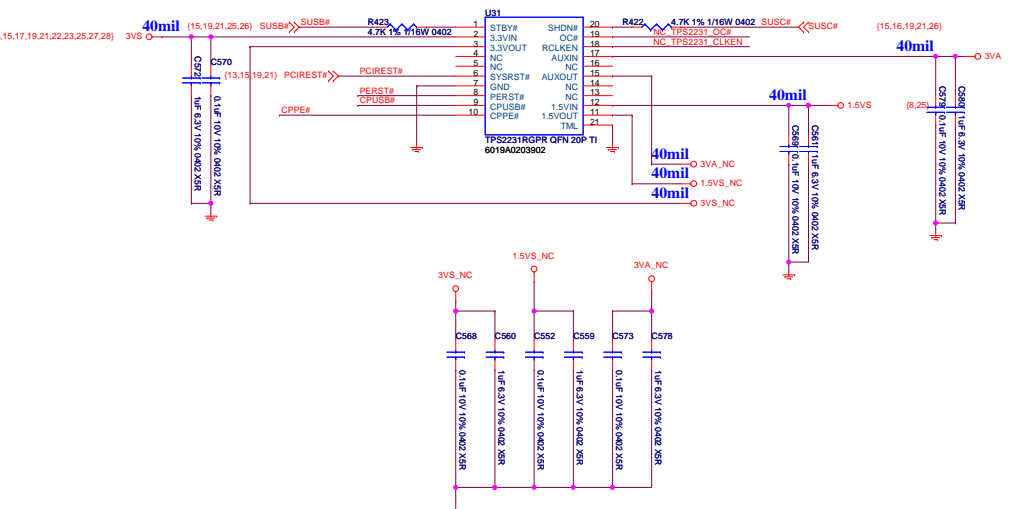
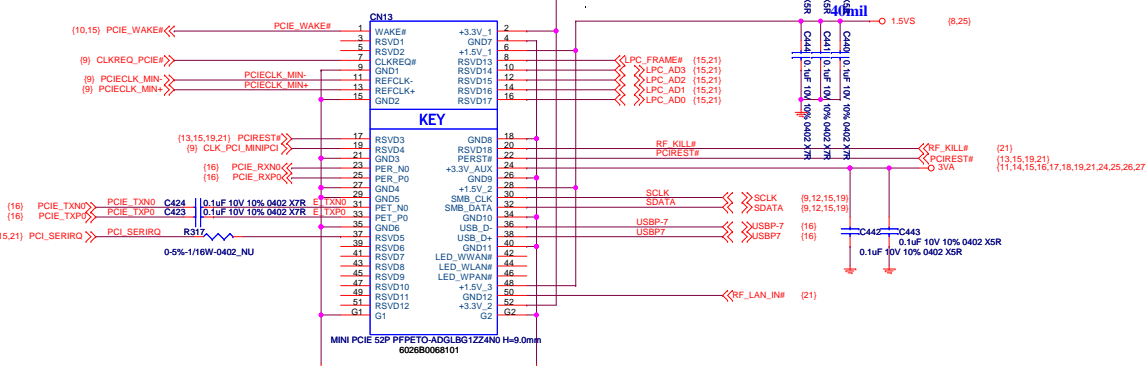
Doc#<OrgAdd>

Title	SiS 968 (3/3)	
Size	Document Number	Rev
C	<Doc>	A02

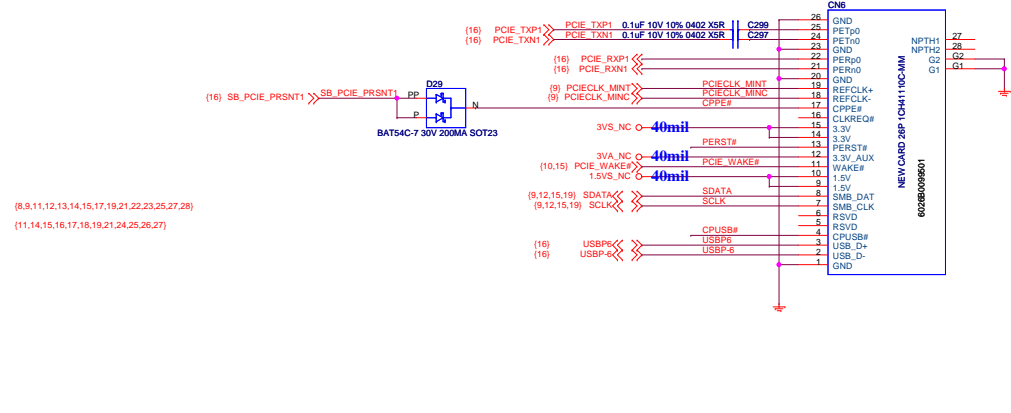
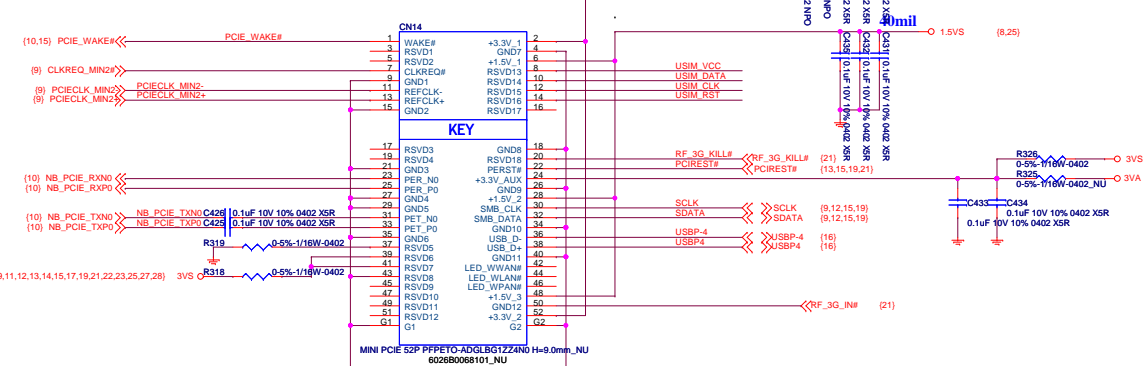
Date: Friday, June 15, 2007 Sheet 17 of 31

Please close to mini card
each pin

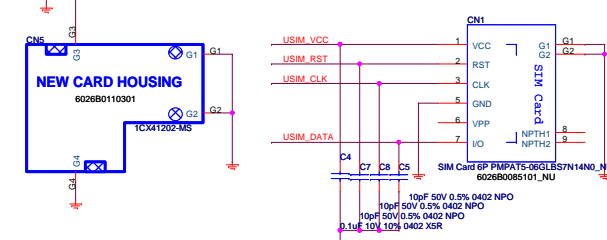
PCIE Mini Card (WLAN)



PCIE Mini Card 2 (3G)



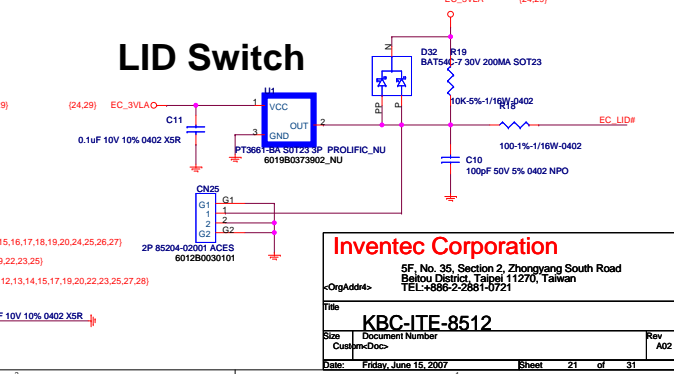
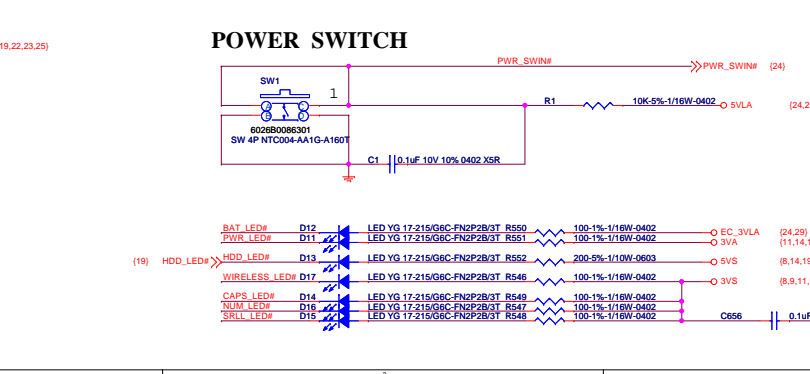
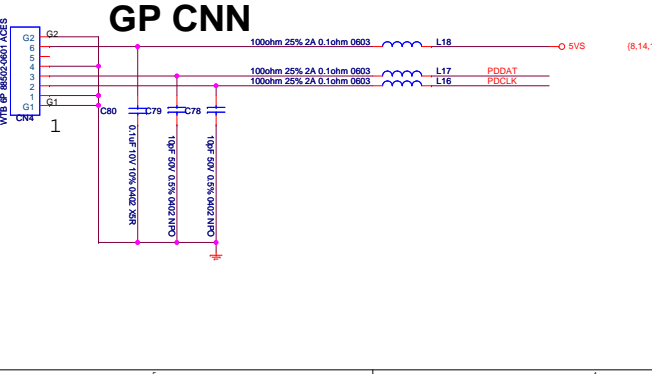
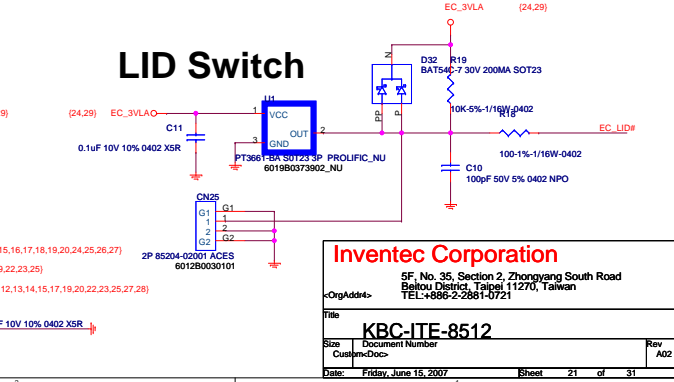
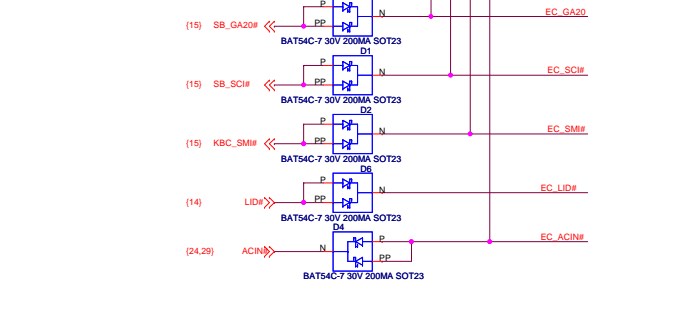
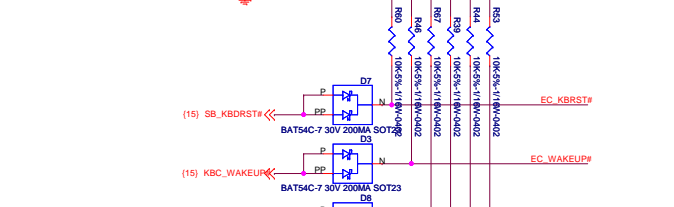
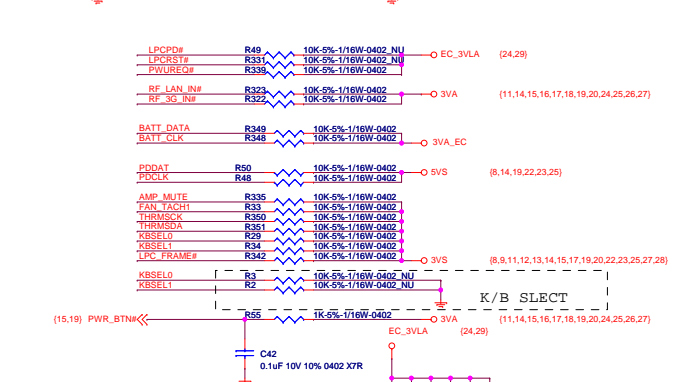
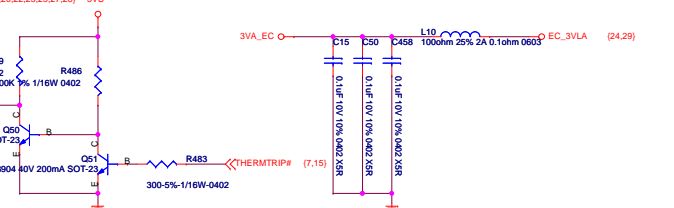
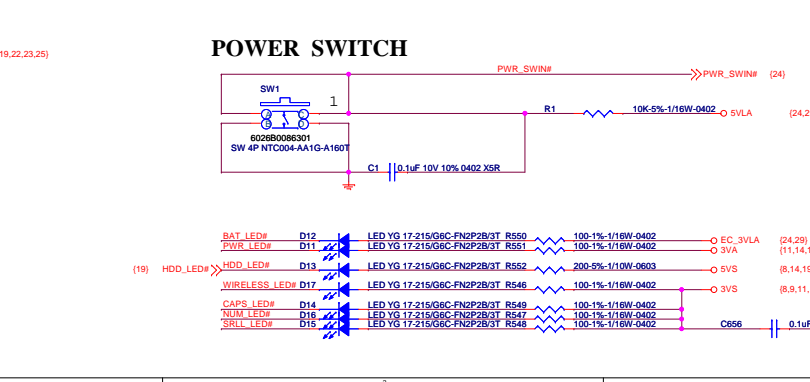
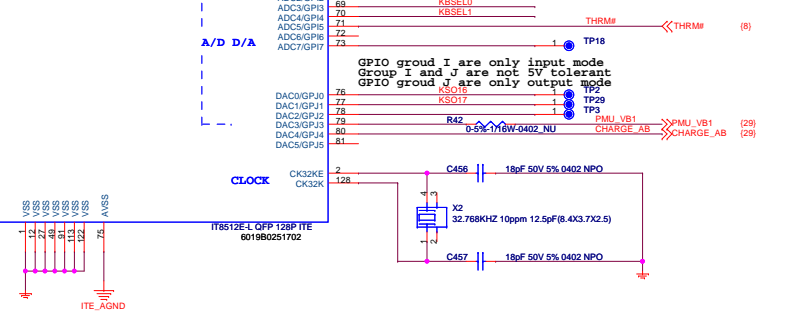
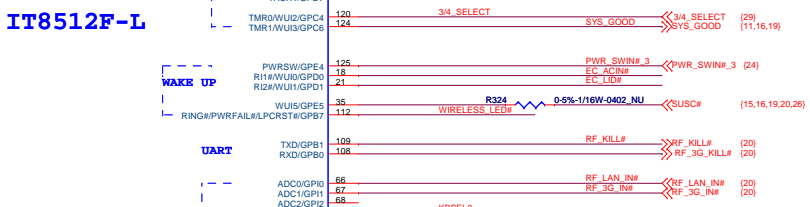
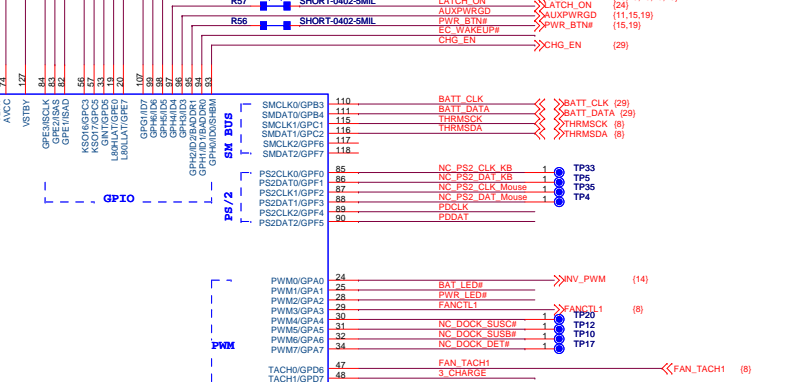
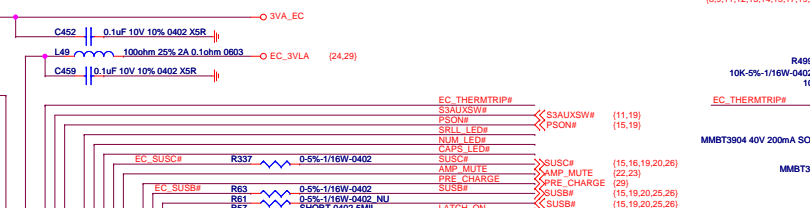
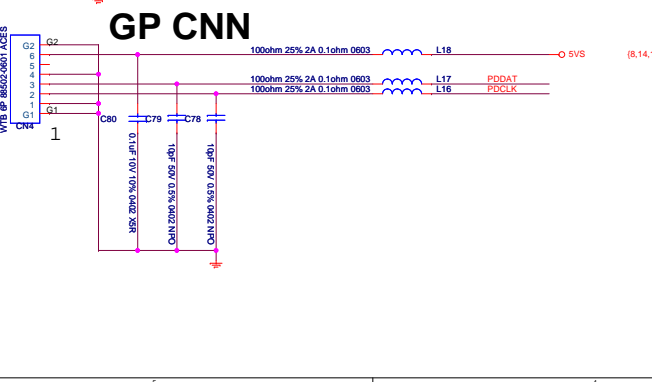
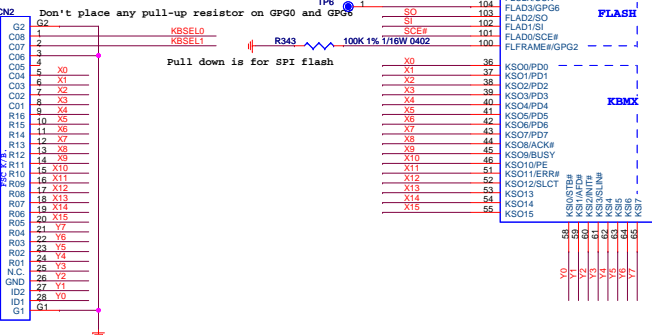
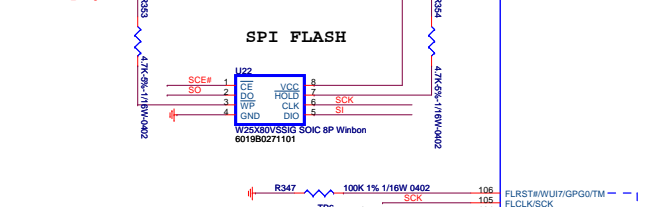
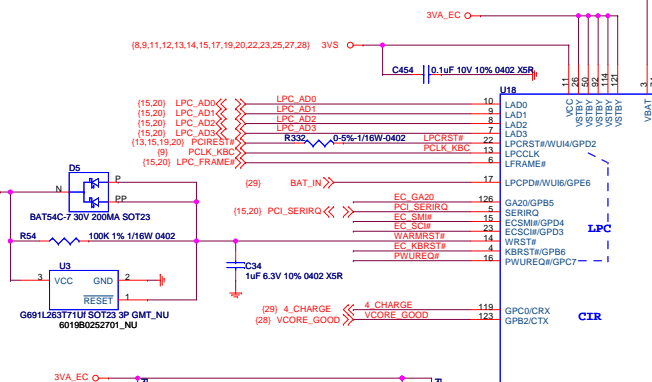
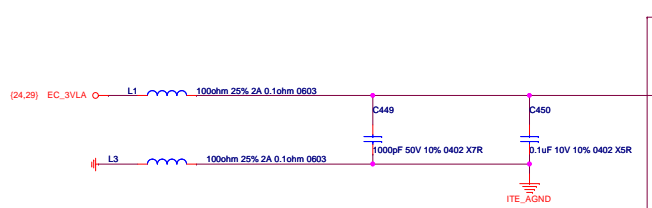
SIM Card.



Inventec Corporation
 5F, No. 35, Section 2, Zhongyuan South Road
 Beitou District, Taipei 11270, Taiwan
 TEL:+886-2-2861-0721

File: **3G/Mini/New card**
 Size: Document Number
 Date: Friday, June 15, 2007

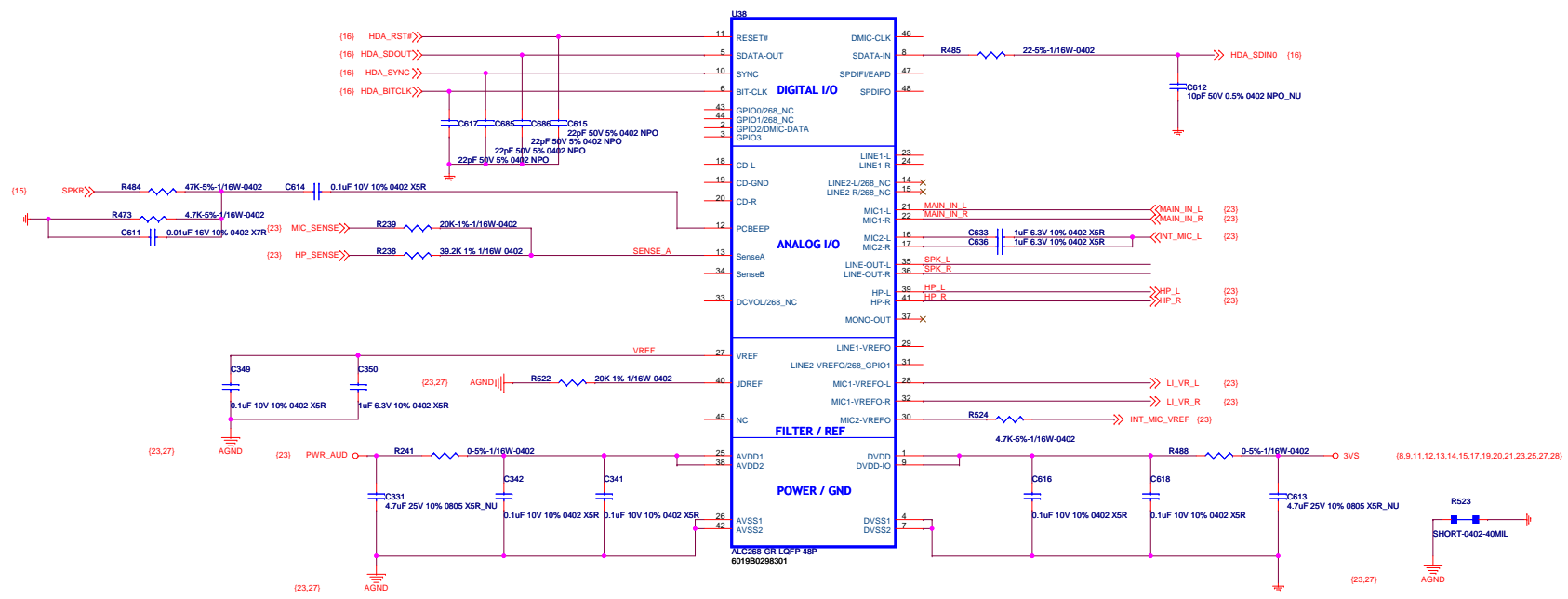
Sheet 20 of 31



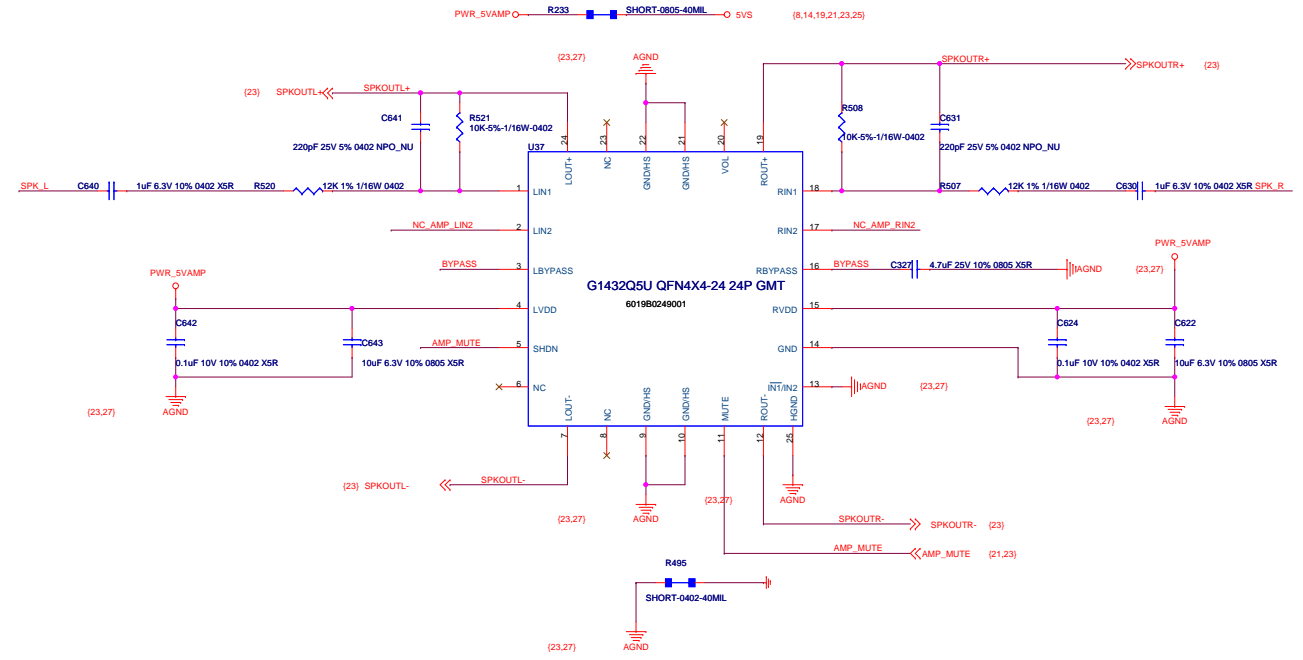
Inventec Corporation
 5F, No. 35, Section 2, Zhongyuan South Road
 Beitou District, Taipei 11270, Taiwan
 TEL:+886-2-2861-0721

File: **KBC-ITE-8512**
 Size: Document Number
 Date: Friday, June 15, 2007

Sheet 21 of 31

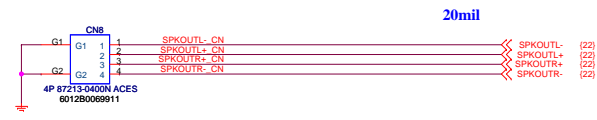
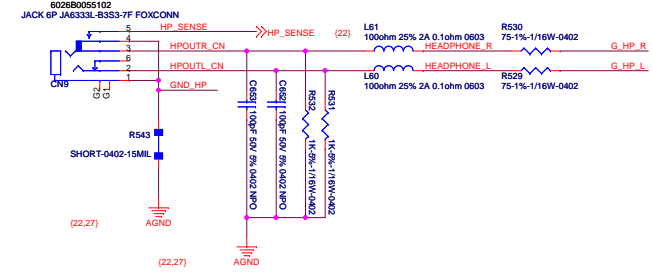
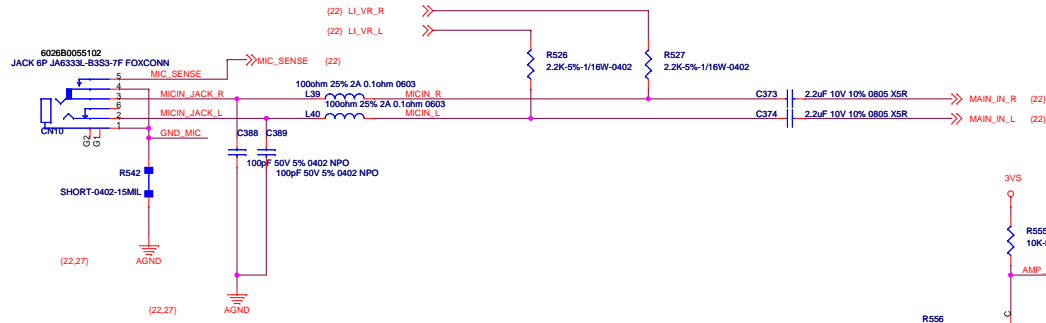


20mil

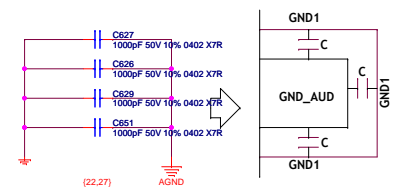
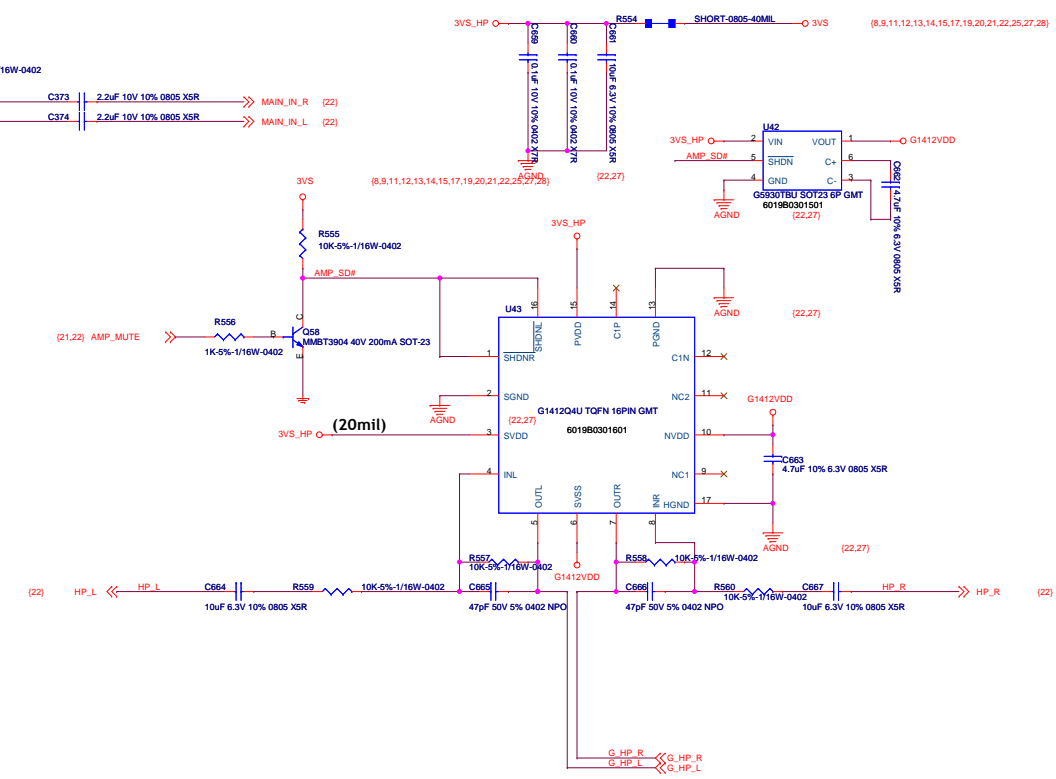
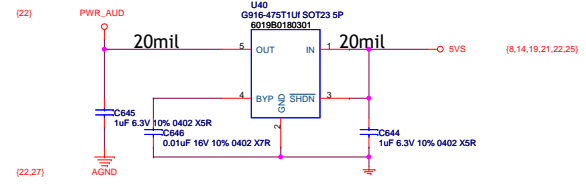
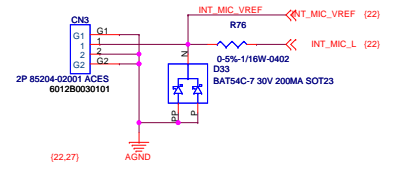


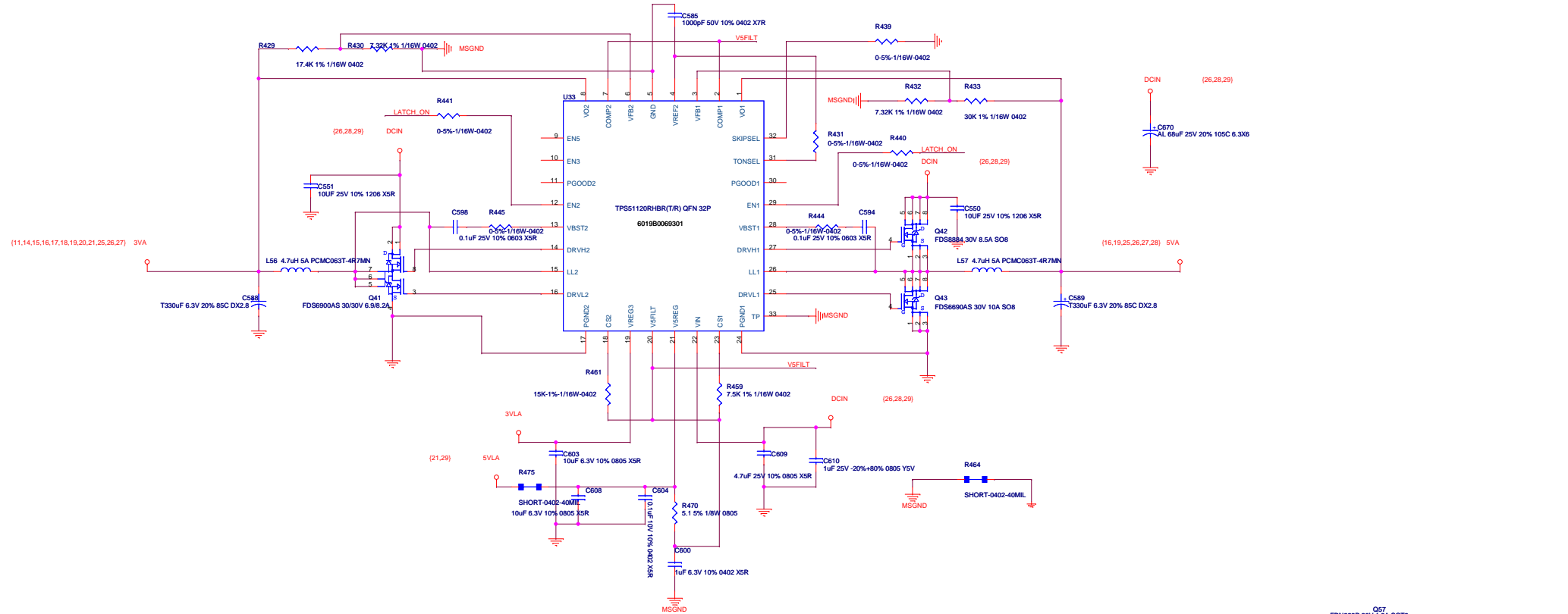
Inventec Corporation
 5F, No. 35, Section 2, Zhongyong South Road
 Beitou District, Taipei 11270, Taiwan
 TEL:+886-2-2861-0721

Originals:
 Title: **AUDIO ALIC268/AMP**
 Size: Document Number
 CustomDoc: Rev: 402
 Date: Friday, June 15, 2007 Sheet 22 of 31



INT_MIC_CN

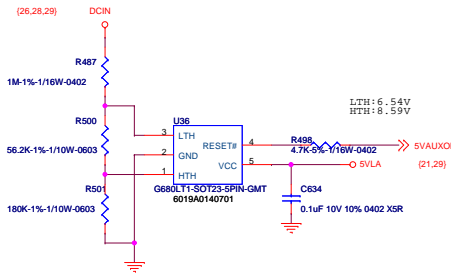




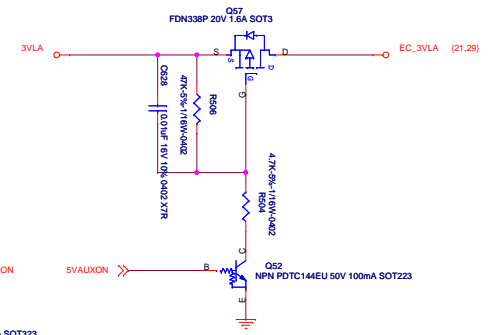
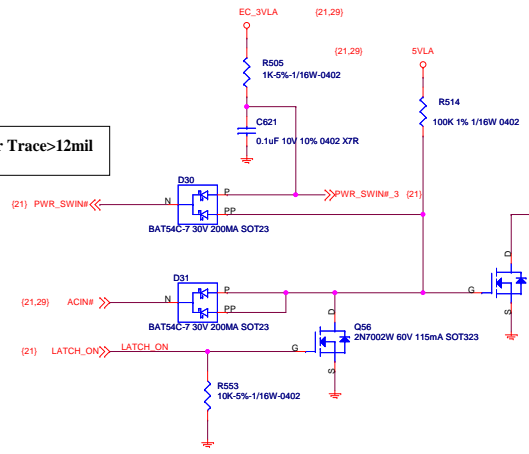
(11,14,15,16,17,18,19,20,21,25,26,27) 3VA

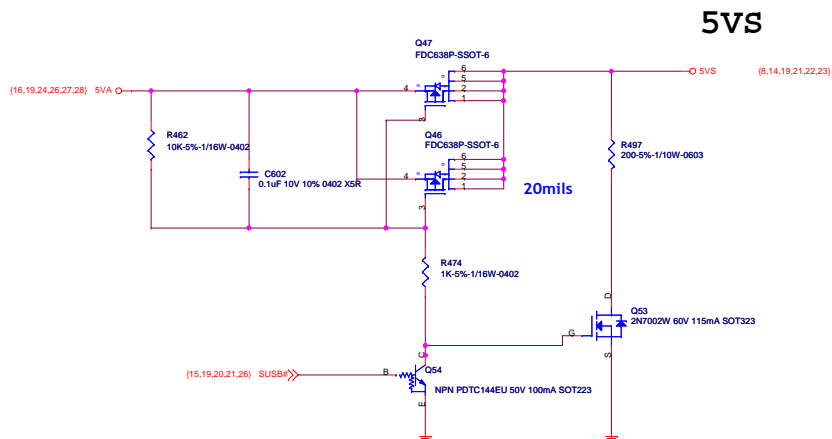
DCIN (26.28,29)

(16,19,25,26,27,28) 5VA

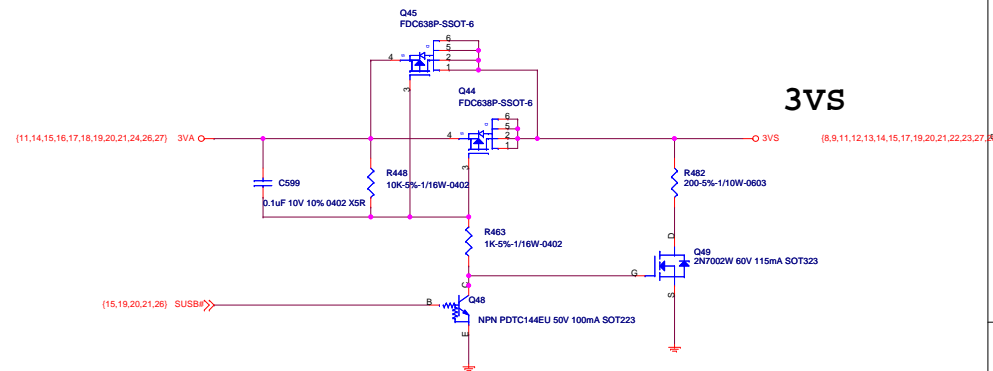


Other Trace>12mil

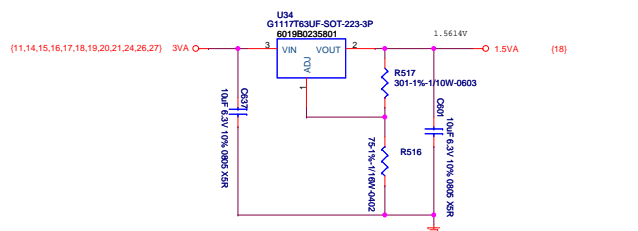




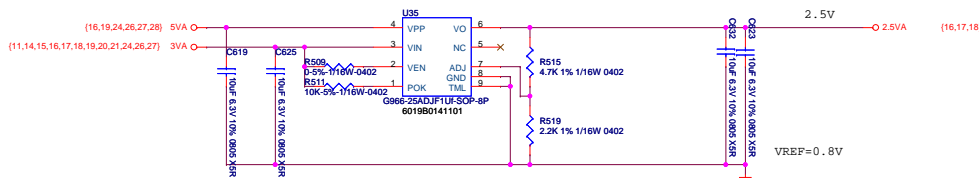
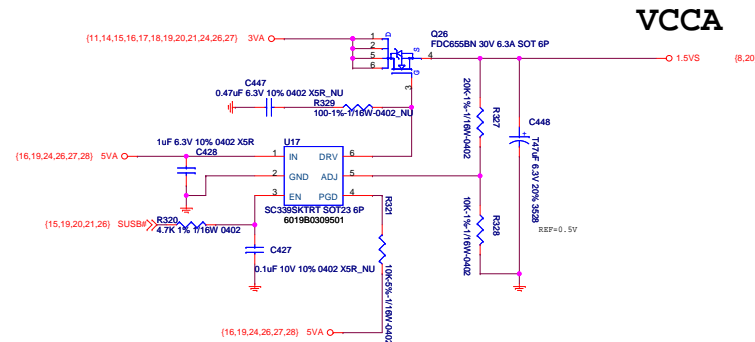
For LAN POWER



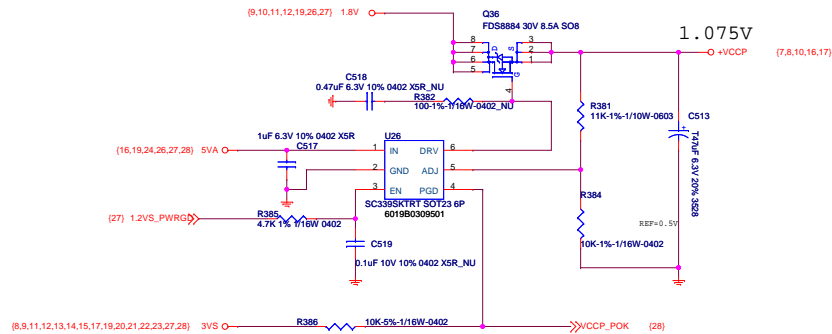
For CPU I/O POWER



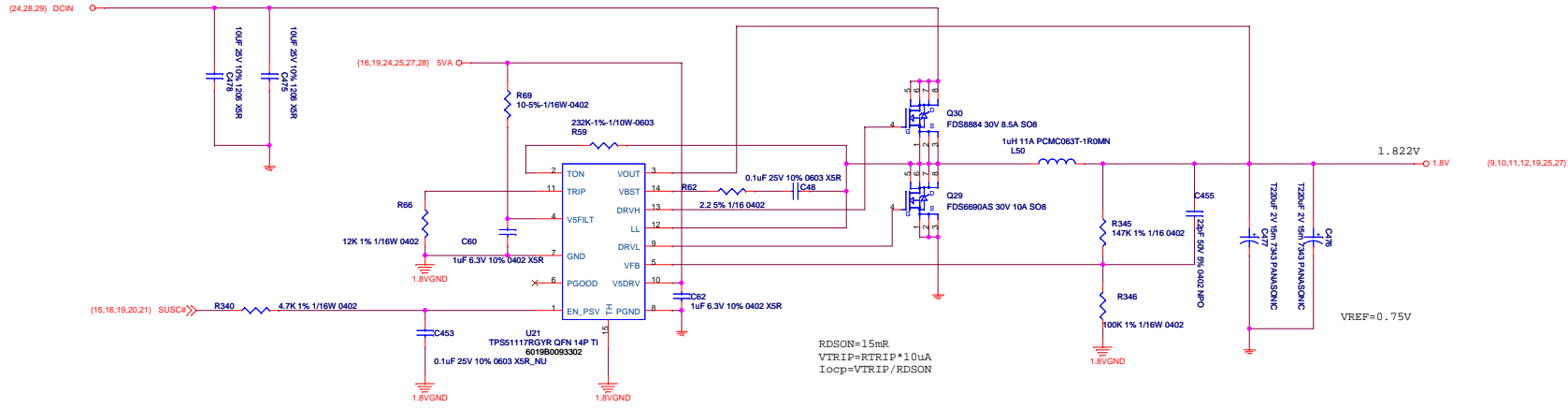
REF=1.25V



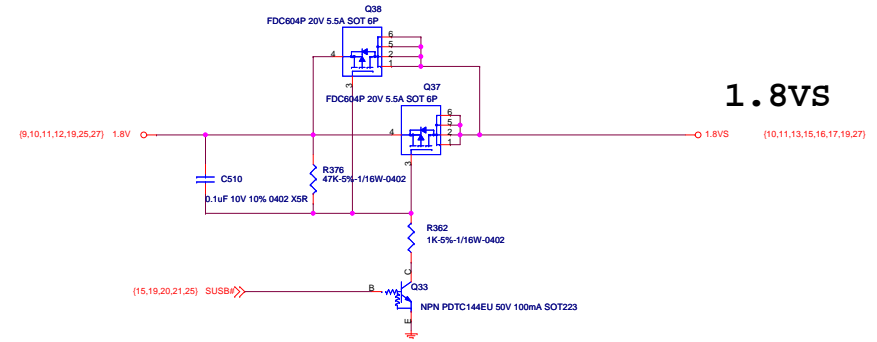
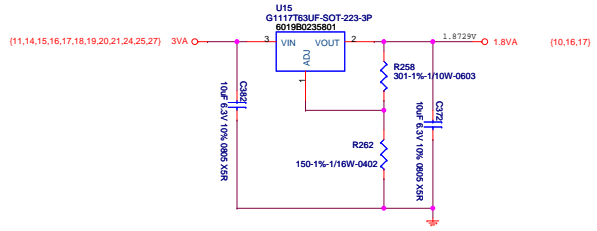
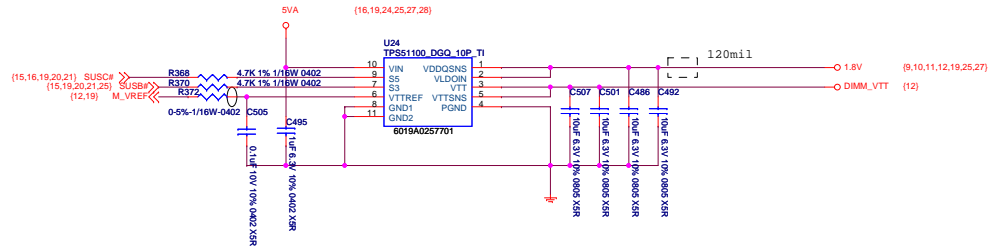
VREF=0.8V



DDR POWER

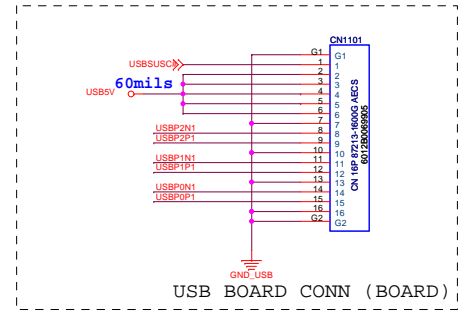
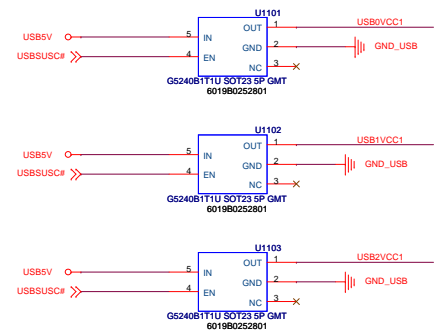
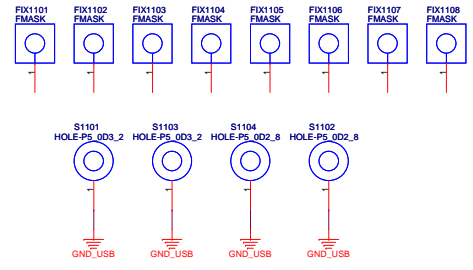
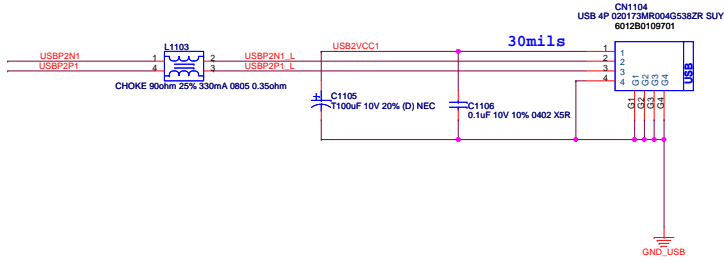
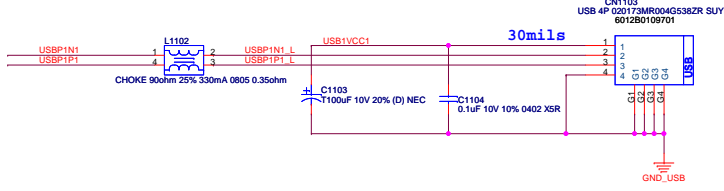
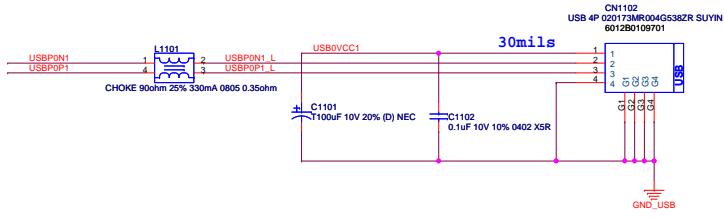


2 Vias **2 Vias**

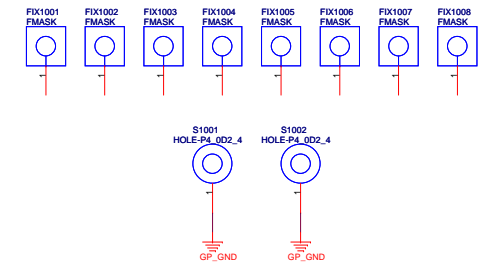
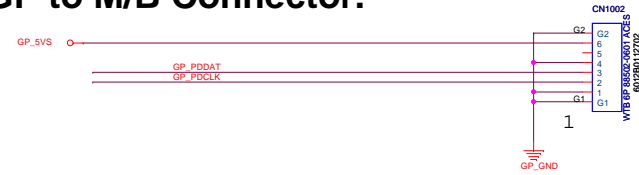


EMI request

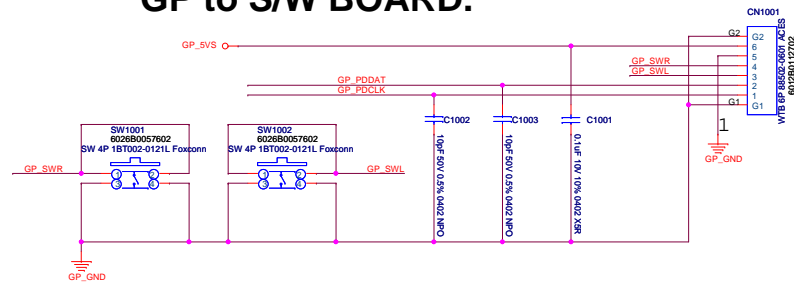
C486	0.1uF 10V 10% 0402 XSR
C487	0.1uF 10V 10% 0402 XSR
C488	0.1uF 10V 10% 0402 XSR
C489	0.1uF 10V 10% 0402 XSR
C490	0.1uF 10V 10% 0402 XSR
C491	0.1uF 10V 10% 0402 XSR
C492	0.1uF 10V 10% 0402 XSR
C493	0.1uF 10V 10% 0402 XSR
C494	0.1uF 10V 10% 0402 XSR
C495	0.1uF 10V 10% 0402 XSR
C496	0.1uF 10V 10% 0402 XSR
C497	0.1uF 10V 10% 0402 XSR
C498	0.1uF 10V 10% 0402 XSR
C499	0.1uF 10V 10% 0402 XSR
C500	0.1uF 10V 10% 0402 XSR
C501	0.1uF 10V 10% 0402 XSR
C502	0.1uF 10V 10% 0402 XSR
C503	0.1uF 10V 10% 0402 XSR
C504	0.1uF 10V 10% 0402 XSR
C505	0.1uF 10V 10% 0402 XSR
C506	0.1uF 10V 10% 0402 XSR
C507	0.1uF 10V 10% 0402 XSR
C508	0.1uF 10V 10% 0402 XSR
C509	0.1uF 10V 10% 0402 XSR
C510	0.1uF 10V 10% 0402 XSR



GP to M/B Connector.



GP to S/W BOARD.



Inventec Corporation

5F, No. 35, Section 2, Zhongyong South Road
 Beitou District, Taipei 11270, Taiwan
 TEL: +886-2-2861-0721

Title		Rev
GP S/W BOARD		A02
Size	Document Number	
Customer/Doc		
Date: Friday, June 15, 2007	Sheet	31 of 31