

RJK0305DPB

Silicon N Channel Power MOS FET Power Switching

REJ03G1353-0900 Rev.9.00 Apr 19, 2006

Features

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance $R_{DS(on)}\!=6.7~\text{m}\Omega~\text{typ.}~(\text{at}~V_{GS}=10~\text{V})$

Outline

RENESAS Package code: PTZZ0005DA-A (Package name: LFPAK)

5
0
1, 2, 3 Source
4 Gate
5 Drain

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	30	V
Gate to source voltage	V_{GSS}	+16/-12	V
Drain current	I _D	30	А
Drain peak current	I _{D(pulse)} Note1	120	А
Body-drain diode reverse drain current	I _{DR}	30	А
Avalanche current	I _{AP} Note 2	10	A
Avalanche energy	E _{AR} Note 2	10	mJ
Channel dissipation	Pch Note3	45	W
Channel to Case Thermal Resistance	θch-C	2.78	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

- 2. Value at Tch = 25°C, Rg \geq 50 Ω
- 3. $Tc = 25^{\circ}C$

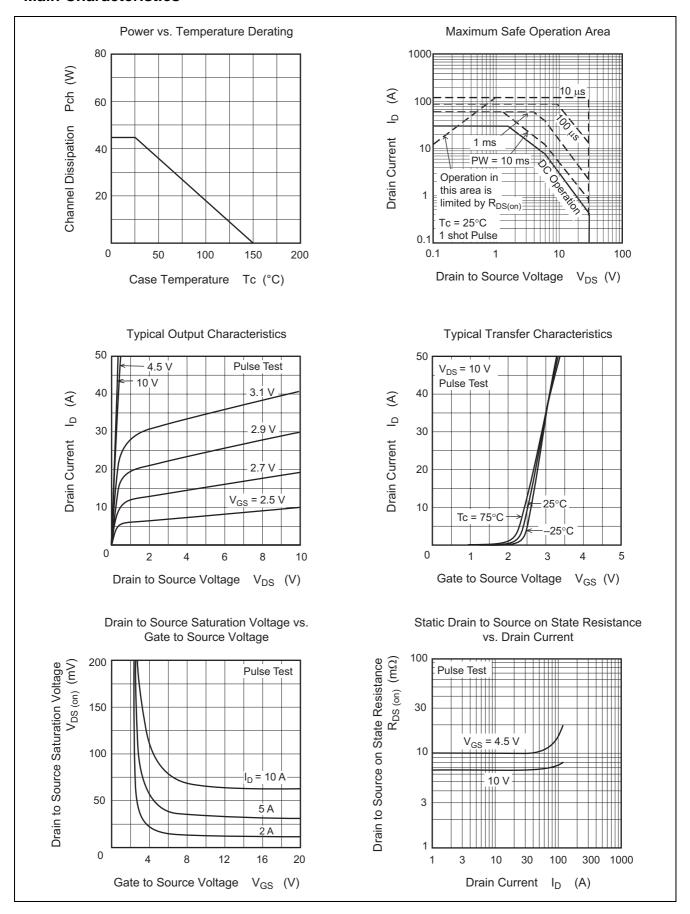
Electrical Characteristics

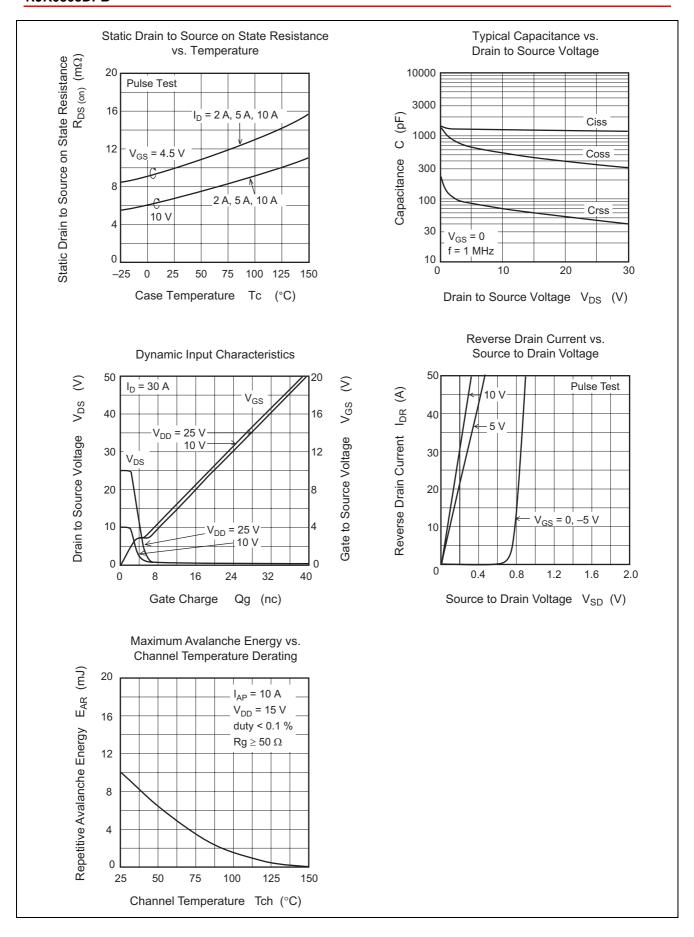
 $(Ta = 25^{\circ}C)$

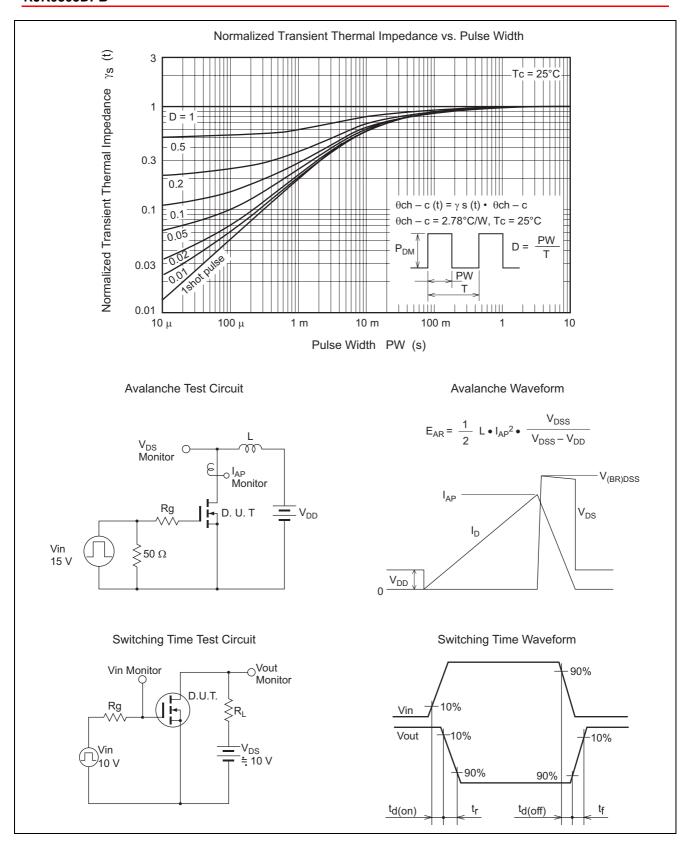
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	_	_	± 0.1	μΑ	$V_{GS} = +16/-12 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	1	_	1	μΑ	$V_{DS} = 30 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.2	_	2.5	٧	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state	R _{DS(on)}	1	6.7	8.0	mΩ	$I_D = 15 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance	R _{DS(on)}	1	10	13	mΩ	$I_D = 15 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y _{fs}	_	45	_	S	$I_D = 15 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	1250	_	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0,$
Output capacitance	Coss	_	530	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss	_	70	_	pF	
Gate Resistance	Rg	_	0.6	_	Ω	
Total gate charge	Qg	_	8	_	nC	$V_{DD} = 10 \text{ V}, V_{GS} = 4.5 \text{ V},$
Gate to source charge	Qgs	_	3.6	_	nC	I _D = 30 A
Gate to drain charge	Qgd	_	1.5	_	nC	1
Turn-on delay time	t _{d(on)}	_	7.0	_	ns	$V_{GS} = 10 \text{ V}, I_D = 15 \text{ A},$
Rise time	t _r	_	3.0	_	ns	$V_{DD} \cong 10 \text{ V,R}_L = 0.67 \Omega,$
Turn-off delay time	t _{d(off)}	_	35	_	ns	$Rg = 4.7 \Omega$
Fall time	t _f	_	3.0	_	ns	
Body-drain diode forward voltage	V_{DF}	_	0.85	1.08	V	$IF = 30 A, V_{GS} = 0^{Note4}$
Body–drain diode reverse recovery time	t _{rr}	_	30	_	ns	IF = 30 A, $V_{GS} = 0$ di _F / dt = 100 A/ μ s

Notes: 4. Pulse test

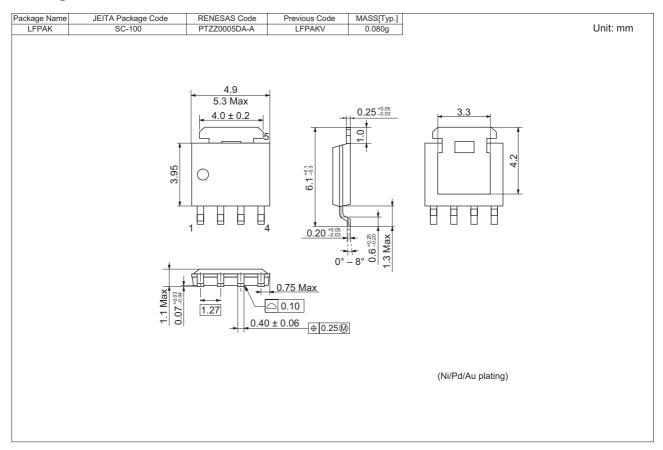
Main Characteristics







Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
RJK0305DPB-00-J0	2500 pcs	Taping

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